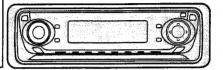
## Pioneer sound.vision.soul

# Service Manual



ORDER NO. CRT2806

MULTI-CD/DAB CONTROL HIGH POWER CD PLAYER WITH RDS TUNER

DEH-P640

R

XN/EW



● This service manual should be used together with the following manual(s):

Model No.	Order No.	Mech. Module	Remarks
CX-977	CRT2624	S9	CD Mech. Module:Circuit Description, Mech.Description, Disassembly

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For details, refer to "Important symbols for good services".

PIONEER CORPORATION
4-1, Meguro 1-Chome, Meguro-ku, Tokyo 153-8654, Japan PIONEER ELECTRONICS (USA) INC. P.O.Box 1760, Long Beach, CA 90801-1760 U.S.A. PIONEER EUROPE NV Haven 1087 Keetberglaan 1, 9120 Melsele, Belgium PIONEER ELECTRONICS ASIACENTRE PTE.LTD. 253 Alexandra Road, #04-01, Singapore 159936

[ Important symbols for good services ]

In this manual, the symbols shown-below indicate that adjustments, settings or cleaning should be made securely. When you find the procedures bearing any of the symbols, be sure to fulfill them:

#### 1. Product safety



You should conform to the regulations governing the product (safety, radio and noise, and other regulations), and should keep the safety during servicing by following the safety instructions described in this manual.

#### 2. Adjustments



To keep the original performances of the product, optimum adjustments or specification confirmation is indispensable. In accordance with the procedures or instructions described in this manual, adjustments should be performed.

#### 3. Cleaning



For optical pickups, tape-deck heads, lenses and mirrors used in projection monitors, and other parts requiring cleaning, proper cleaning should be performed to restore their performances.

#### 4. Shipping mode and shipping screws



To protect the product from damages or failures that may be caused during transit, the shipping mode should be set or the shipping screws should be installed before shipping out in accordance with this manual, if necessary.

#### 5. Lubricants, glues, and replacement parts



Appropriately applying grease or glue can maintain the product performances. But improper lubrication or applying glue may lead to failures or troubles in the product. By following the instructions in this manual, be sure to apply the prescribed grease or glue to proper portions by the appropriate amount. For replacement parts or tools, the prescribed ones should be used.

## CD Player Service Precautions



- For pickup unit(CXX1480) handling, please refer to "Disassembly" (see page 47)
   During replacement, handling precautions shall be taken to prevent an electrostatic discharge (protection by a jumper-solder).
- During disassembly, be sure to turn the power off since an internal IC might be destroyed when a connector is plugged or unplugged.
- 3. Please checking the grating after changing the service pickup unit(see page 41).

## 1. SAFETY INFORMATION

This service manual is intended for qualified service technicians; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.

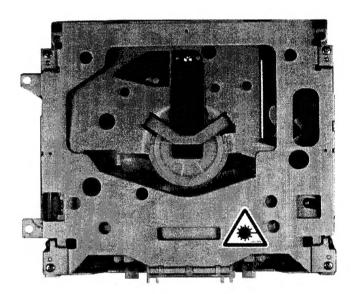
Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

- 1. Safety Precautions for those who Service this Unit.
- When checking or adjusting the emitting power of the laser diode exercise caution in order to get safe, reliable results.

#### Caution:

- 1. During repair or tests, minimum distance of 13cm from the focus lens must be kept.
- 2. During repair or tests, do not view laser beam for 10 seconds or longer.
- 2. A "CLASS 1 LASER PRODUCT" label is affixed to the bottom of the player.
- 3. The triangular label is attached to the mechanism unit frame.



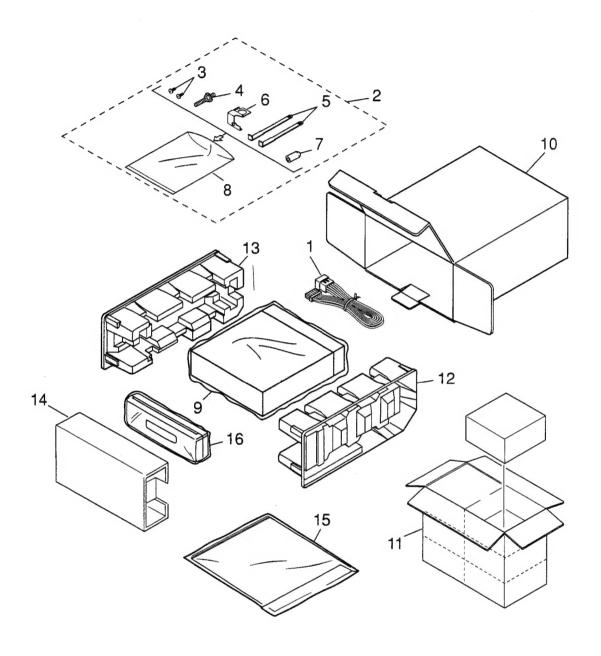


#### 4. Specifications of Laser Diode

Specifications of laser radiation fields to which human access is possible during service. Wavelength = 800 nanometers

## 2. EXPLODED VIEWS AND PARTS LIST

## 2.1 PACKING



### NOTE:

- Parts marked by "\*" are generally unavailable because they are not in our Master Spare Parts List.
- $\bullet$  Screws adjacent to  $\nabla$  mark on the product are used for disassembly.

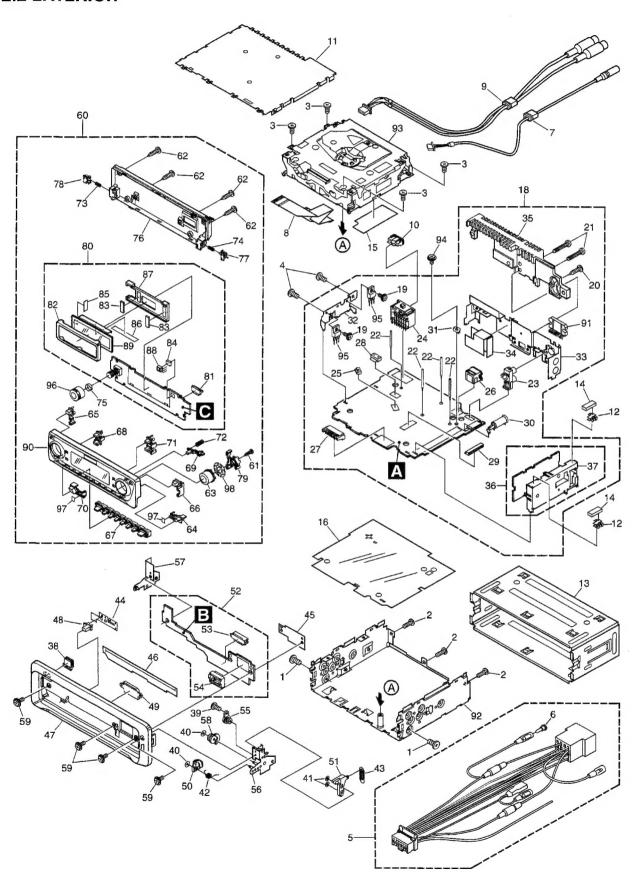
### **PACKING SECTION PARTS LIST**

Mark	No.	Description	Part No.	Mark	No.	Description	Part No.
	1	Cord Assy	CDE6435		15-2	Owner's Manual	CRD3514
	2		CEA3062		15-3	Owner's Manual	CRD3515
	3		BPZ20P060FZK		15-4	Installation Manual	CRD3529
	4	Screw	CBA1002	*	15-5	Caution Card	CRP1207
	5	Handle	CNC5395	*	15-6	Passport	CRY1013
	6	Earth Plate	CNC9450	*	15-7	Warranty Card	CRY1157
	7	Bush	CNV3930		15-8	Polyethylene Bag	CEG1116
*	8	Polyethylene Bag	E36-615		16	Case Assy	CXB3520
	9	Polyethylene Bag	CEG-162				
	10	Carton	CHG4628				
	11	Contain Box	CHL46283				
	12	Protector	CHP2251				
	13	Protector	CHP2252				
	14	Inner Box	CHW1754				
	15-1	Owner's Manual	CRD3513				

Owner's Manual, Installation Manual

- OWING SIVIGING	Owner Striation, installation manage						
Part No.	Language						
CRD3513	English, Spanish						
CRD3514	German, French						
CRB3515	Italian, Dutch						
CRD3529	English, Spanish, German, French, Italian, Dutch						

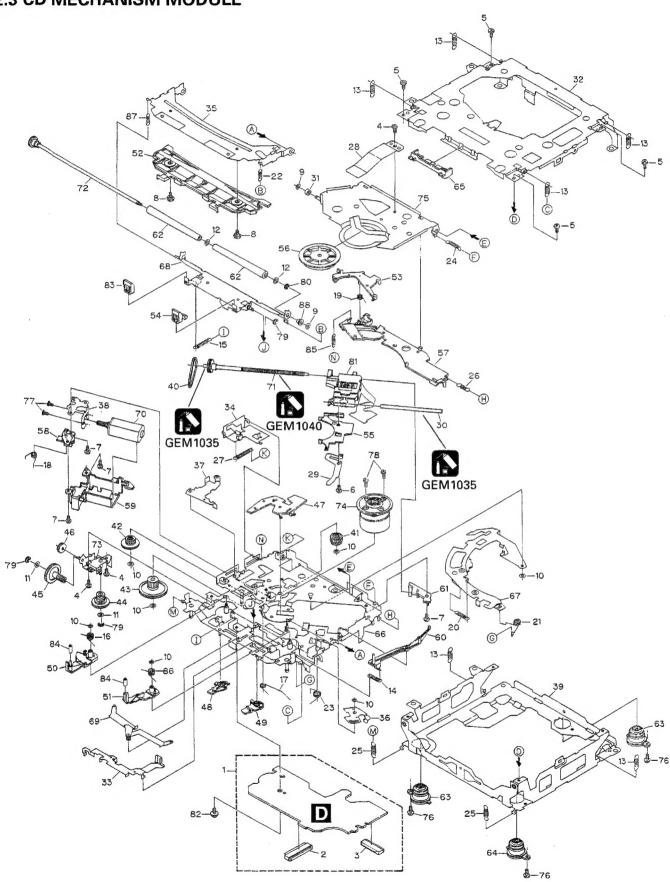
## 2.2 EXTERIOR



### **EXTERIOR SECTION PARTS LIST**

viark	No.	Description	Part No.	Mark	No.	Description	Part No.
	1	Screw	BMZ30P040FZK			Arm	CNV6508
	2	Screw	BMZ30P100FMC		52	Panel Unit	CWM7986
	3	Screw	BSZ26P060FMC		53	Socket(CN1850)	CKS3550
		Screw	BSZ30P040FMC		54	Connector(CN1851)	CKS4462
	5	Cord Assy	CDE6435			Damper Unit	CXB5070
							0)/00050
		Cap	CKX-003			Holder Unit	CXB6356
			CDE6717			Holder Unit	CXB6357
	8	Cable	CDE6714		58		CXB6358
	9	Cord Assy	CDE6454			Screw	IMS20P045FZK
	10	Fuse(10A)	CEK1136		60	Detach Grille Assy	CXB7914
	11	Case	CNB2686		61	Screw	BPZ20P080FMC
		Holder	CNC5704			Screw	BPZ20P100FZK
		Holder	CNC8659			Button(SELECT)	CAC7220
		Cushion	CNM4870		64		CAC7221
		Insulator	CNM7214		-	Button(TA)	CAC7223
	15	modiator	OTTIMIZE 14				0, (0, 220
		Insulator	CNM7629			Button(AUDIO)	CAC7224
	17	••••			67	Button(1-6)	CAC7225
	18	Tuner Amp Unit	CWM7984		68	Button(DISP)	CAC7226
	19	Screw	ASZ26P060FMC	•	69	Button(OPEN)	CAC7227
	20	Screw	BPZ26P120FMC		70	Button(EQ)	CAC7231
	21	Screw	BSZ26P160FMC		71	Button(FUNC)	CAC7489
			CEF1007			Spring	CBH2630
		Clamper					CBH2431
		Pin Jack(CN351)	CKB1035			Spring	
		Plug(CN901)	CKM1330			Spring	CBH2430
	25	Plug(CN451)	CKS1049		/5	Spring	CBL1470
	26	Connector(CN101)	CKS3408		76	Cover	CNS6740
	27	Plug(CN801)	CKS3537		77	Holder	CNV6505
		Connector(CN352)	CKS3598		78	Holder	CNV6506
		Connector(CN653)	CKS3835		79		CNV6909
		Antenna Jack(CN401)	CKX1056		80		CWM7990
						O(ON4004)	01/04504
		Holder	CNC5399			Connector(CN1901)	CKS4524
		Holder	CNC8615			Holder	CNC9642
	33	Holder	CNC9469			Cushion	CNM6633
	34	Insulator	CNM6949			Cushion	CNM7469
	35	Heat Sink	CNR1583		85	Spacer	CNM7697
	36	FM/AM Tuner Unit	CWE1562		86	Spacer	CNM7698
		Holder	CNC8815			Holder	CNV6910
		Button(EJECT)	CAC6839			IC(IC1903)	TSOP1840SB3V
		Screw(M2x2)	CBA1176			OEL Unit	MXS8017
		Washer	CBF1038			Sub Grille Assy	CXB8815
		Washer	CBF1039			IC(IC301)	PAL007A
		Spring	CBH2428			Chassis Unit	CXB8480
		Spring	CBH2429			CD Mechanism Module(S9ANA	
		Spring	CBL1512			Screw	ISS26P055FUC
	45	Holder	CNC9096		95	Transistor(Q921,999)	2SD2396
	46	Cover	CNM6854	*	96	Knob	CAA2697
		Panel	CNS6930			Spacer	CNM7807
	47						
	48	Pin Lighting Conductor	CNV6486 CNV6487			Cushion	CNM7808

## 2.3 CD MECHANISM MODULE

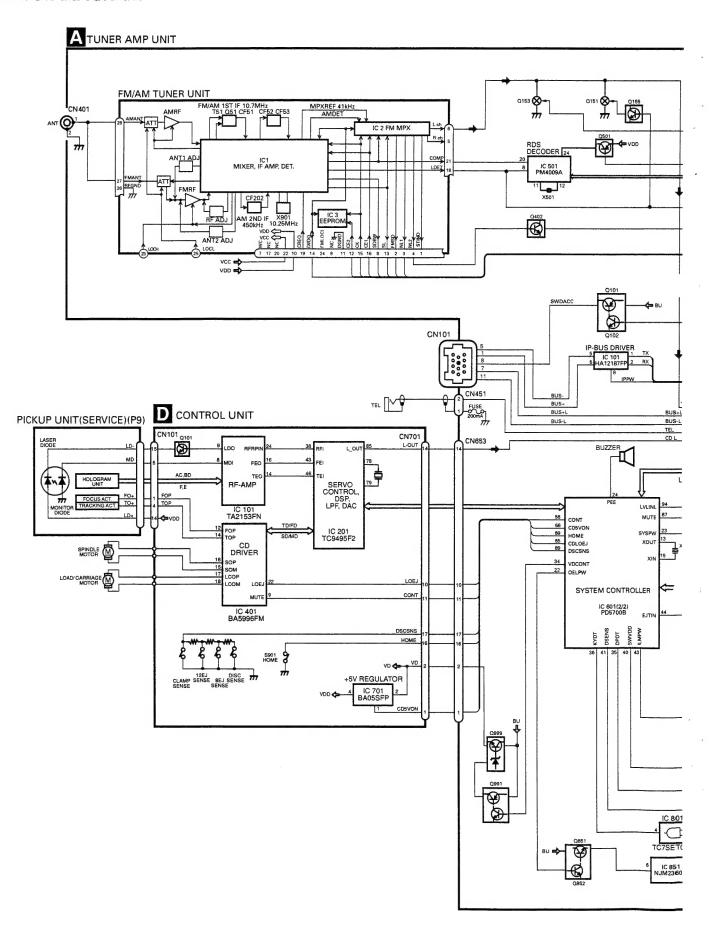


## ● CD MECHANISM MODULE SECTION PARTS LIST

Mark No.	Description	Part No.	Mark N	lo.	Description	Part No.
1	Control Unit	CWX2481		46	Gear	CNV6320
	Connector(CN701)	CKS1959		47	Arm	CNV6322
	Connector(CN101)	CKS3486			Arm	CNV6323
	Screw	BMZ20P025FMC			Arm	CNV6324
	Screw	BSZ20P040FMC			Arm	CNV6888
5	Sciew	D32201 0401 WIC		50	74111	01110000
6	Screw(M2x4)	CBA1362		51	Arm	CNV6889
7	Screw(M2x3)	CBA1527			Guide	CNV6327
8	Screw	CBA1545		53	Arm	CNV6924
9	Washer	CBF1037		54	Guide	CNV6921
10	Washer	CBF1038		55	Rack	CNV6923
11	Washer	CBF1039		56	Clamper	CNV6331
	Washer	CBF1060			Arm	CNV6332
		CBH2378			Guide	CNV6333
	Spring	CBH2379			Cover	CNV6334
	Spring				Arm	CNV6335
15	Spring	CBH2514		ου	Am	CIVVOSSS
16	Spring	CBH2533		61	Guide	CNV6336
	Spring	CBH2382		62	Roller	CNV6338
	Spring	CBH2383		63	Damper	CNV6339
	Spring	CBH2384		64	Damper	CNV6340
	Spring	CBH2527			Guide	CNV6925
		00110000		ee	Channin I Init	CVP7000
	Spring	CBH2386			Chassis Unit	CXB7980
	Spring	CBH2537			Arm Unit	CXB7983
	Spring	CBH2390			Arm Unit	CXB7984
	Spring	CBH2391			Arm Unit	CXB7985
25	Spring	CBH2523		70	Motor Unit(M2)	CXB5903
26	Spring	CBH2426		71	Screw Unit	CXB5904
	Spring	CBH2444		72	Gear Unit	CXB8076
	Spring	CBL1561		73	Bracket Unit	CXB7982
	Spring	CBL1553		74	Motor Unit(M1)	CXB6007
	Shaft	CLA3845			Arm Unit	CXB8504
	Dellas	CLA3910		76	Screw(M2x5)	EBA1028
	Roller	CNC9654			Screw	JFZ20P020FMC
	Frame					JGZ17P020FZK
-	Lever	CNC9664			Screw	
	Lever	CNC8949			Washer	YE15FUC
35	Arm	CNC9661		80	Washer	YE20FUC
36	Arm	CNC9016		81	Pickup Unit(Service)(P9)	CXX1480
	Arm	CNC9017		82	Screw	IMS26P030FMC
	Bracket	CNC9123		83	Guide	CNV6922
	Frame	CNC9656			Roller	CNV6887
	Belt	CNT1086			Spring	CBH2509
		ONIV (0000		00	Coning	CDU12E12
	Gear	CNV6886			Spring	CBH2512
	Gear	CNV6316			Spring	CBH2536
	Gear	CNV6317		88	Collar	CNV6906
	Gear	CNV6318				
45	Gear	CNV6319				

## 3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM

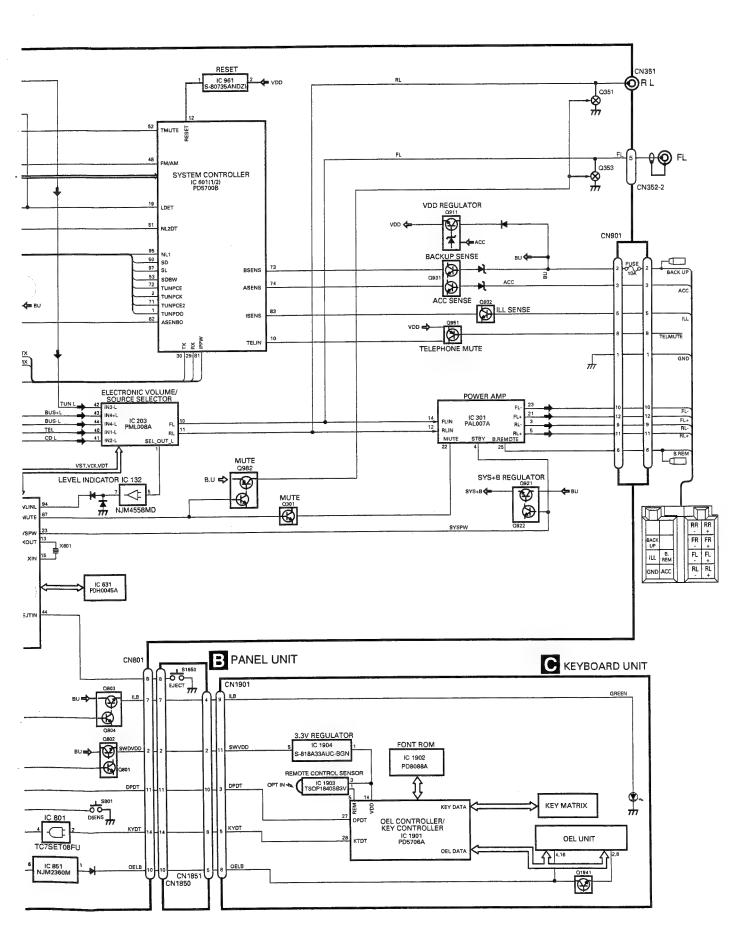
## 3.1 BLOCK DIAGRAM



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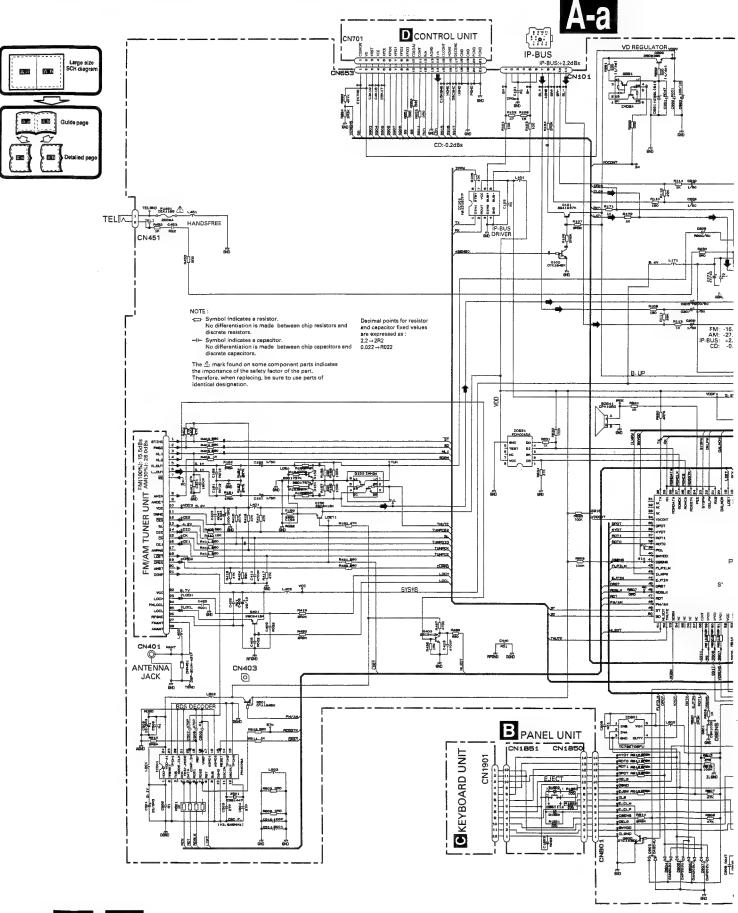
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C

## 3.2 OVERALL CONNECTION DIAGRAM(GUIDE PAGE)

Note: When ordering service parts, be sure to refer to "EXPLODED VIEWS AND PARTS LIST" or "ELECTRICAL PARTS LIST".

3

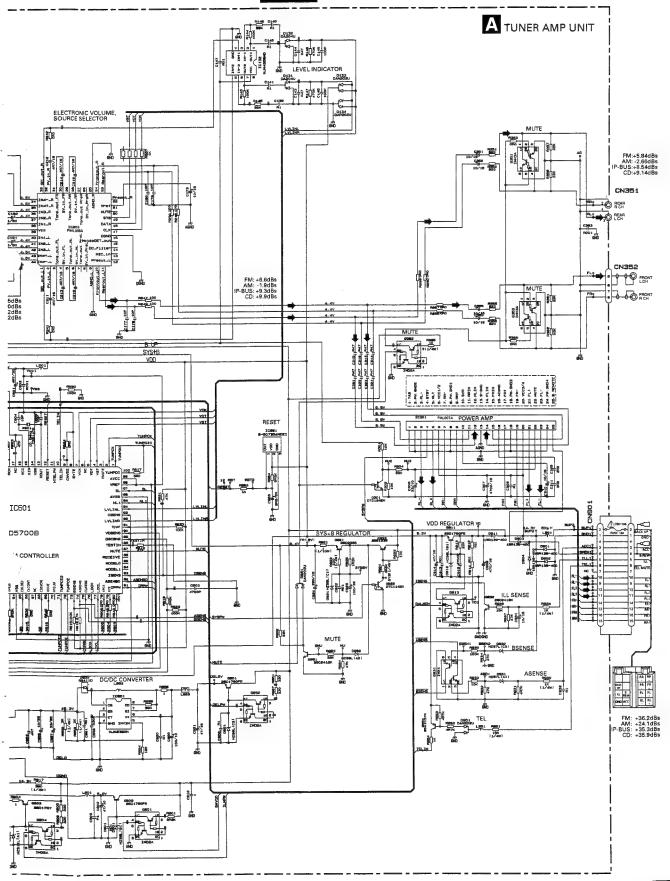


12 A B

2

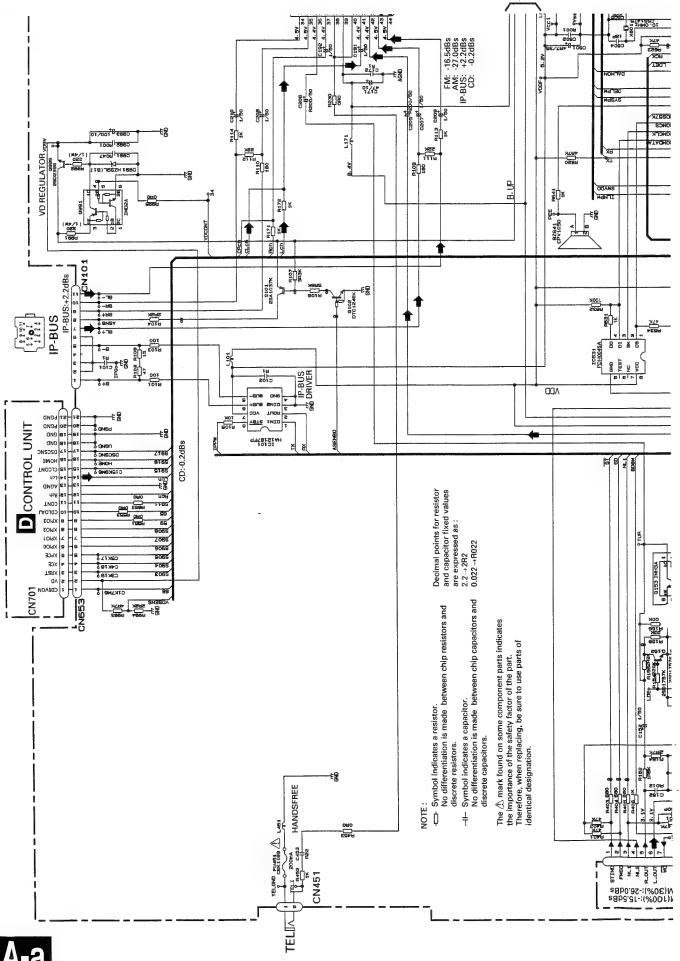
3

A-b



A

13



3

4

14 A-a

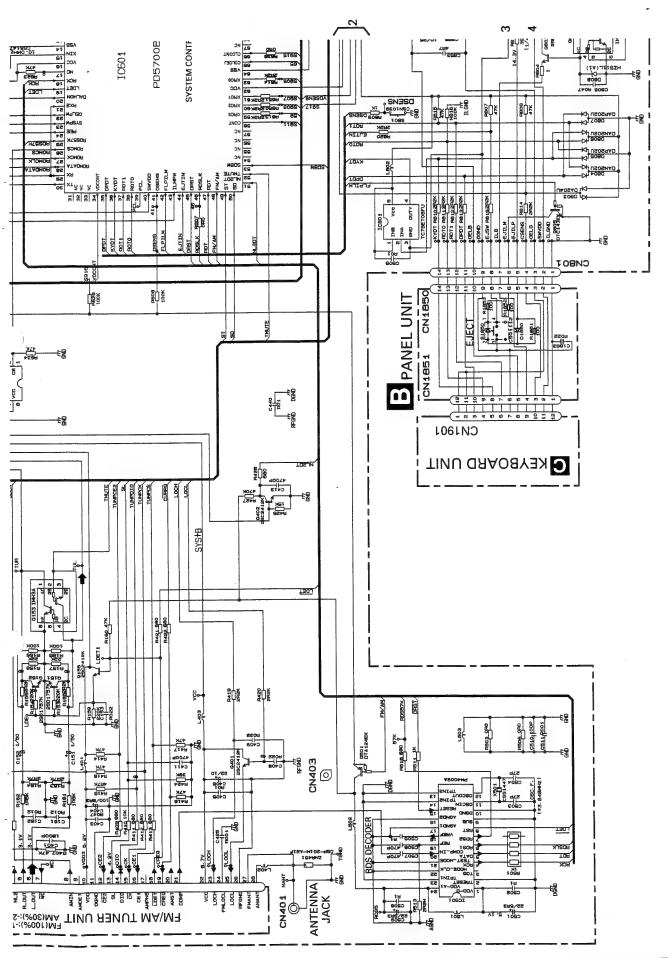
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A-a B

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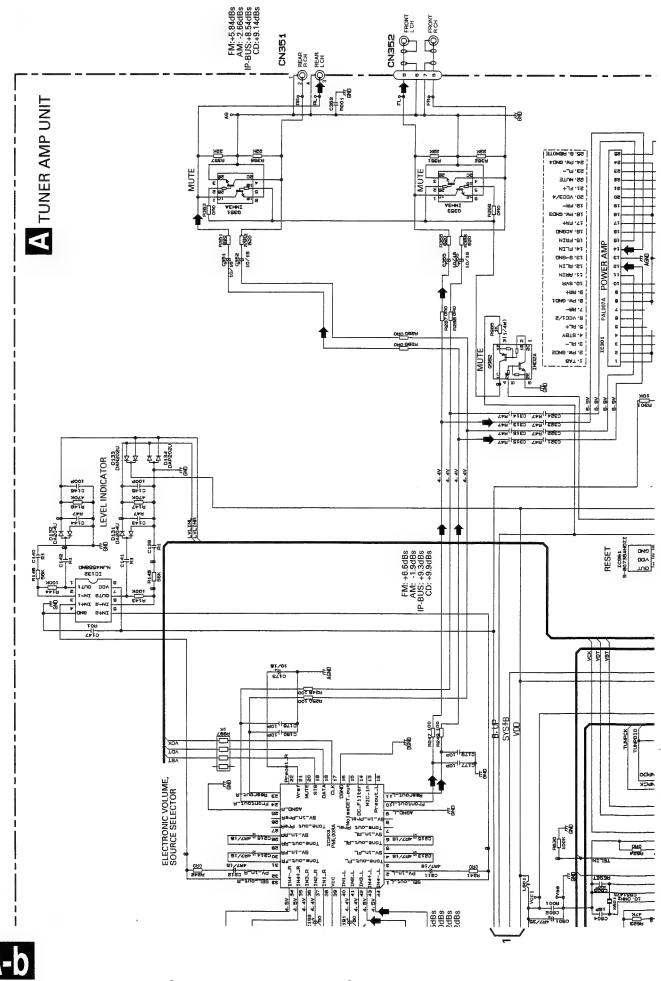
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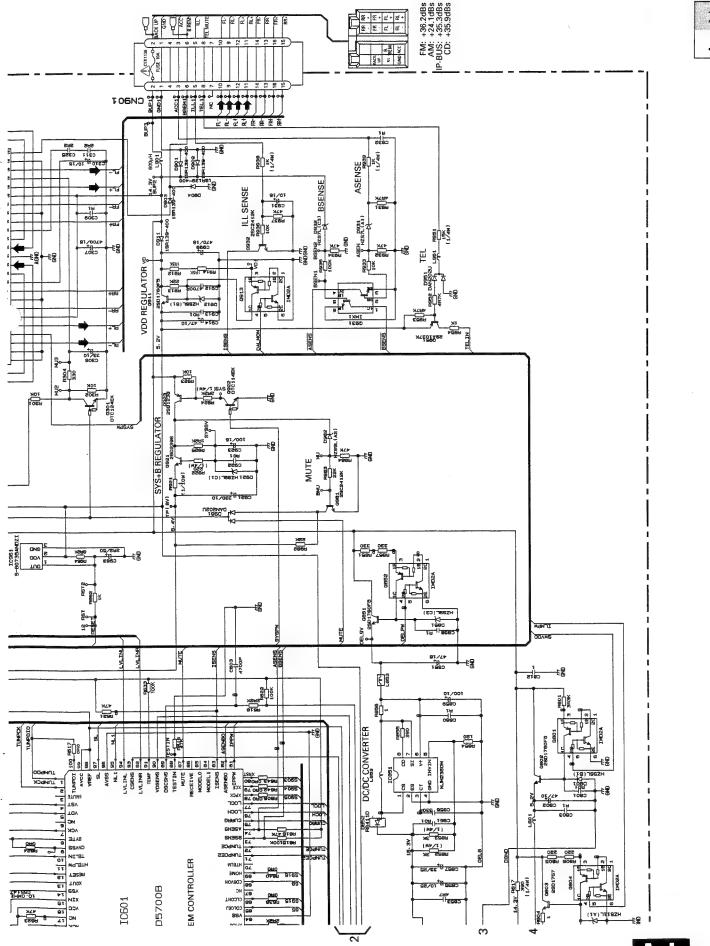
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A-a



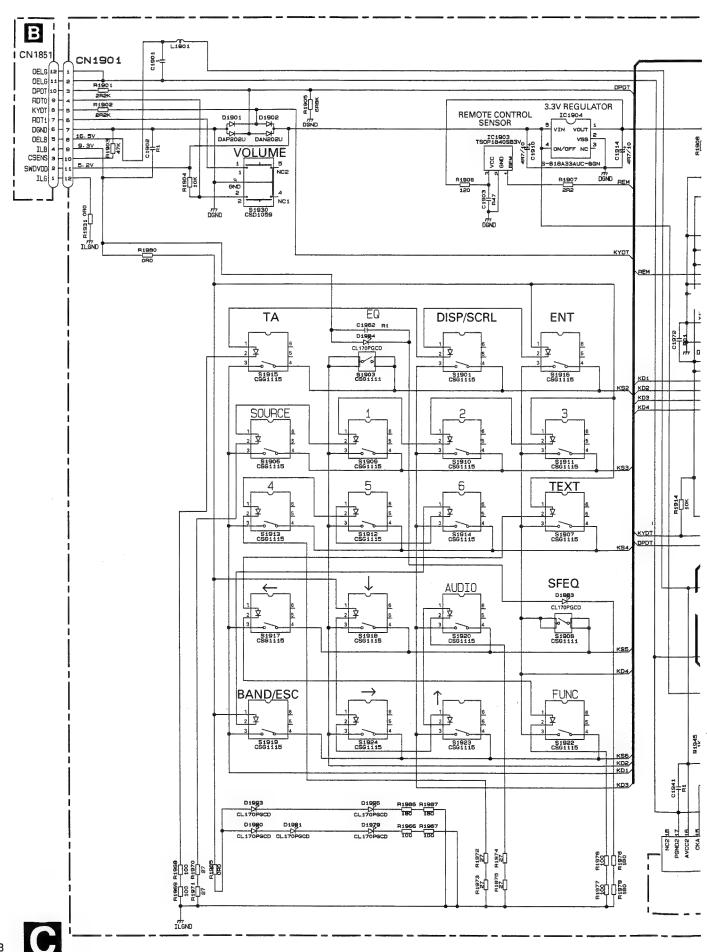


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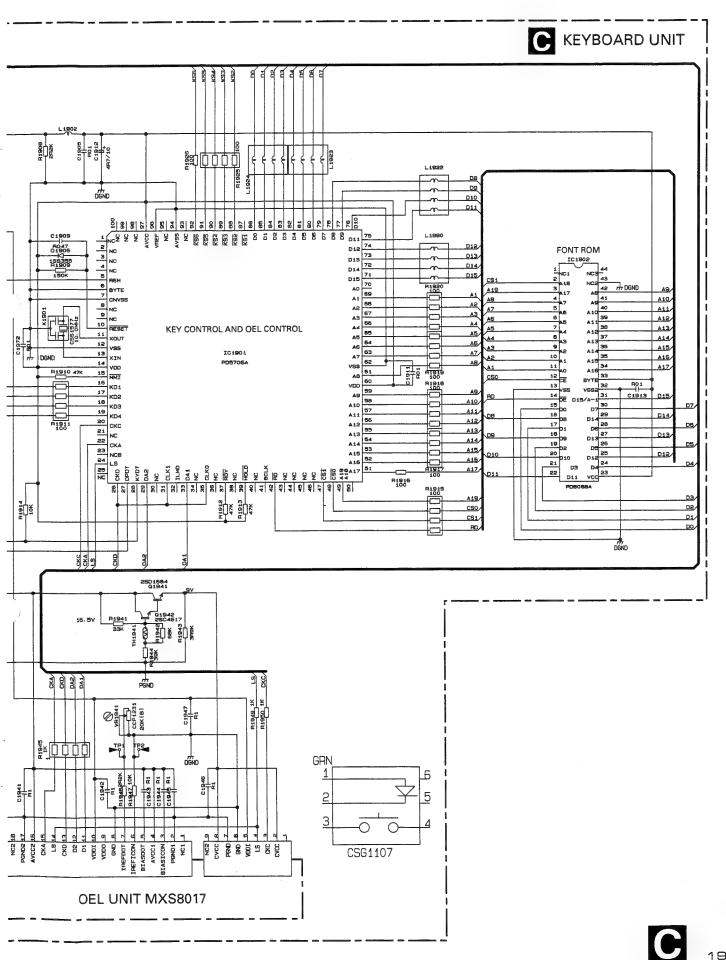
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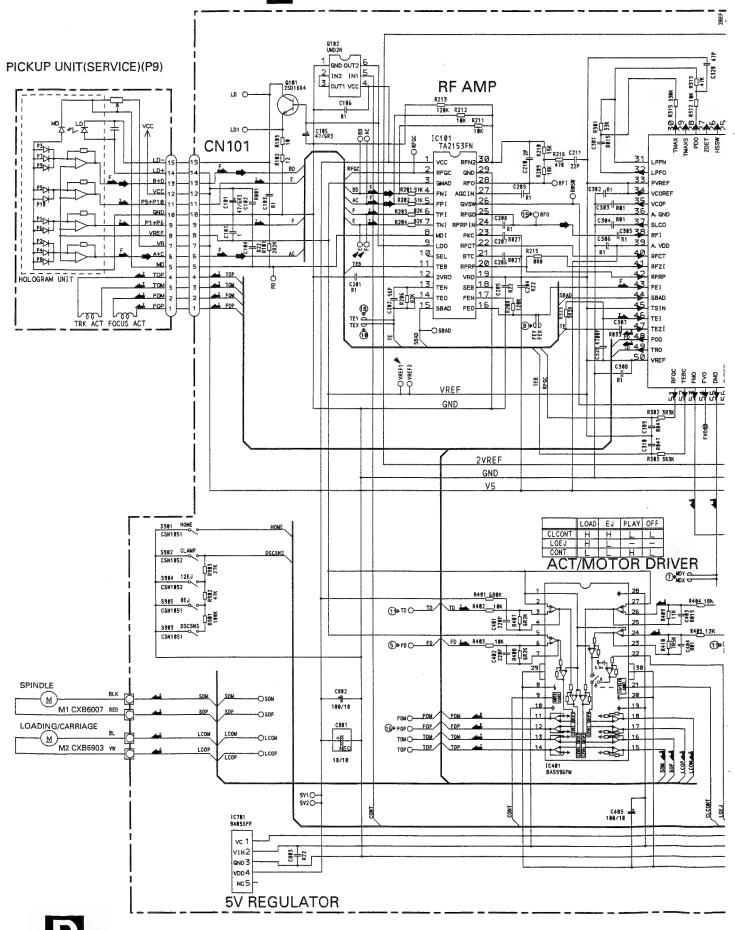
## 3.3 KEYBOARD UNIT

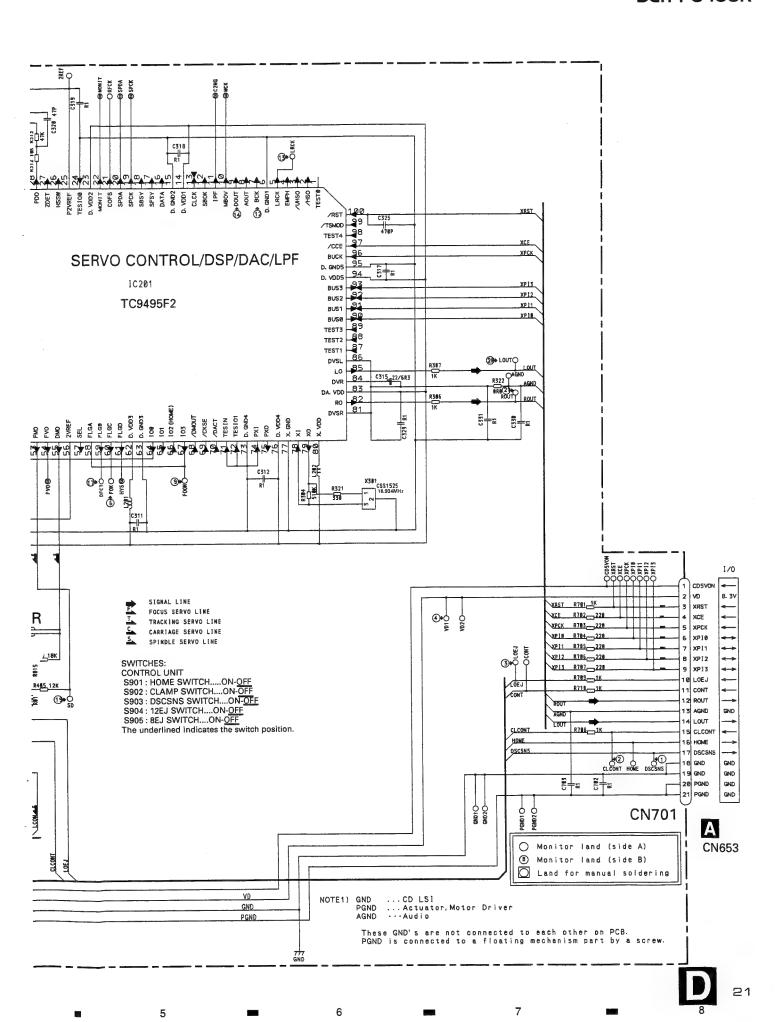


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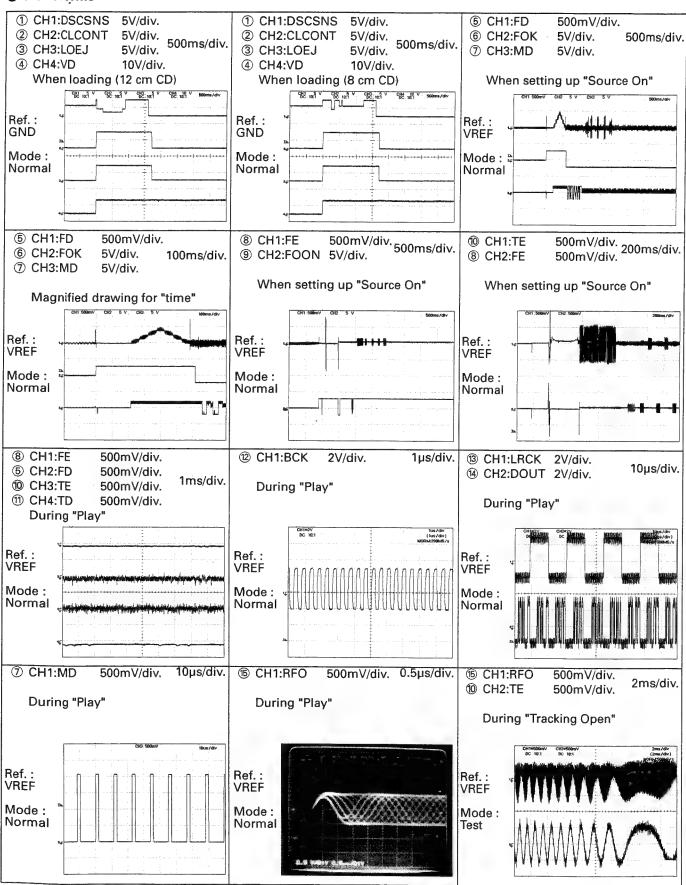




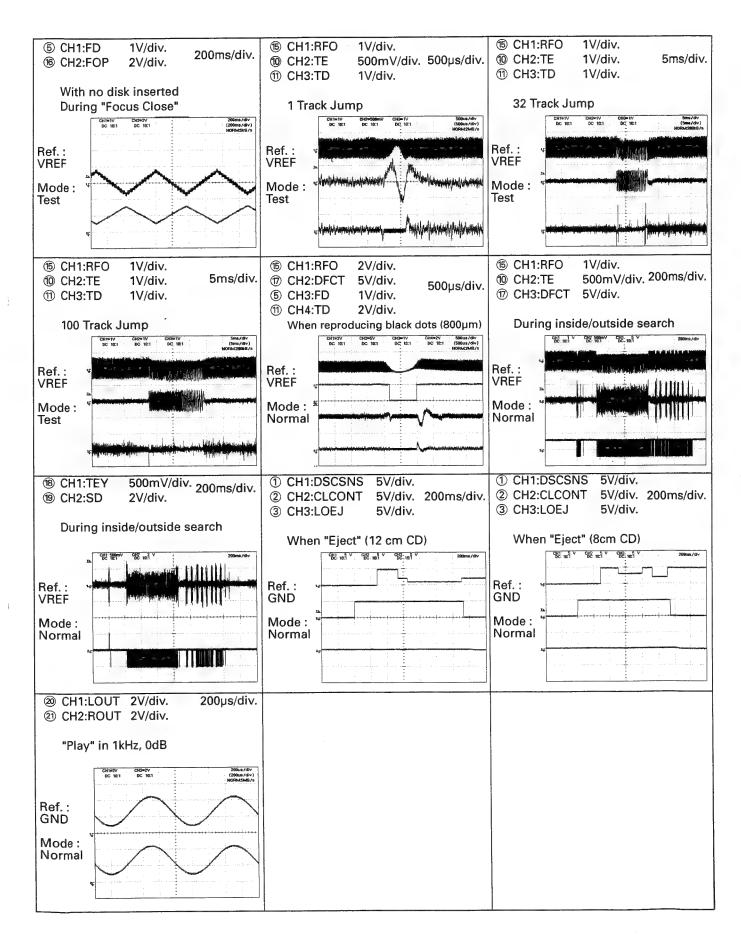
Note:1. The encircled numbers denote measuring pointes in the circuit diagram.

2. Reference voltage

## Waveforms



VREF:2.1V



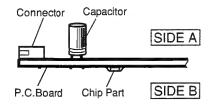
## 4. PCB CONNECTION DIAGRAM

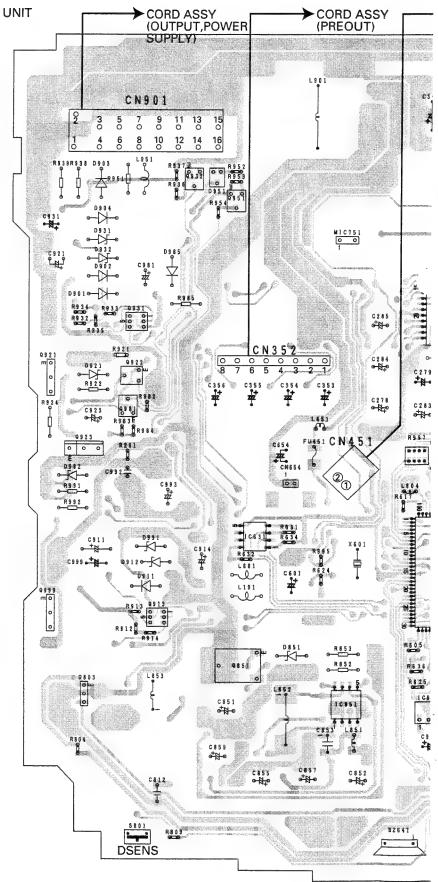
## **4.1 TUNER AMP UNIT**

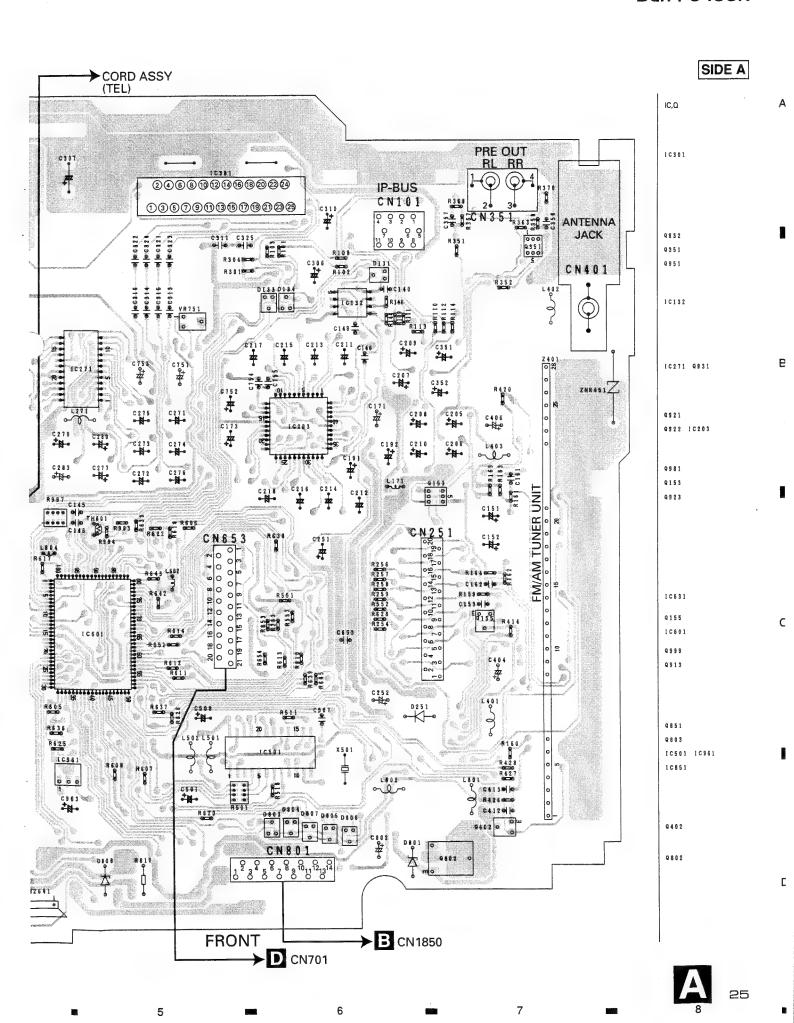
A TUNER AMP UNIT

#### **NOTE FOR PCB DIAGRAMS**

- 1. The parts mounted on this PCB include all necessary parts for several destination. For further information for respective destinations, be sure to check with the schematic dia-
- 2. Viewpoint of PCB diagrams







TUNER AMP UNIT IC,Q Q 1 0 2 Q301 Q101 9982 Q352 Q353 Q 9 8 3 0401 Q 1 5 2 Q154 Q151 Q 9 9 1 Q 9 1 1 IC101 Q852 Q 5 0 1 I C 8 D 1 0804

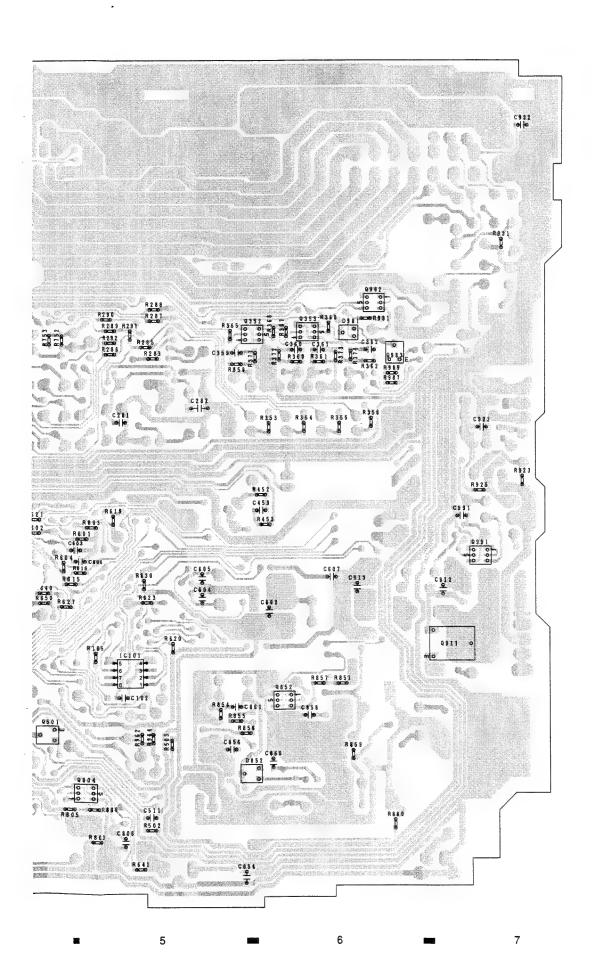
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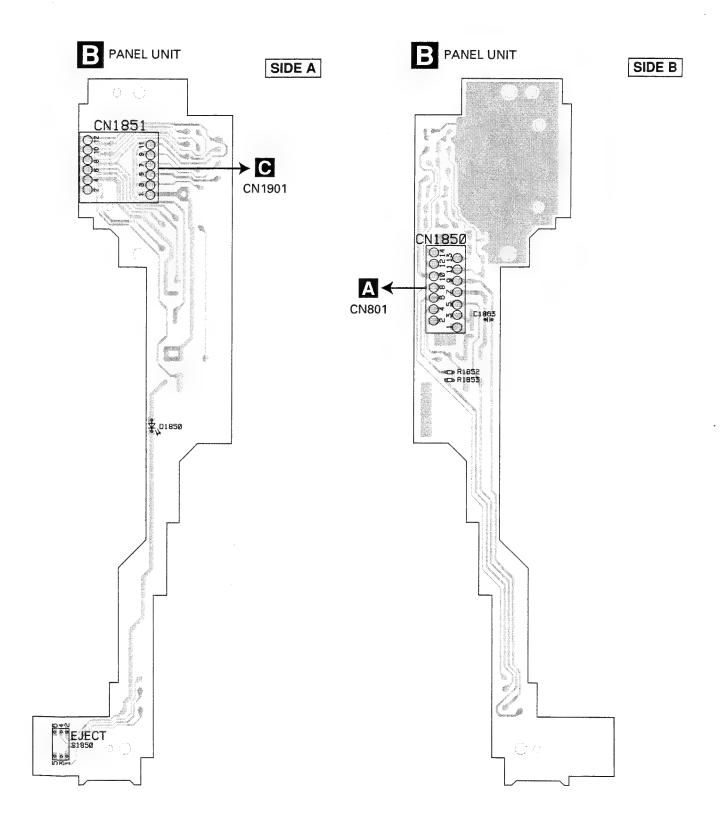
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SIDE B





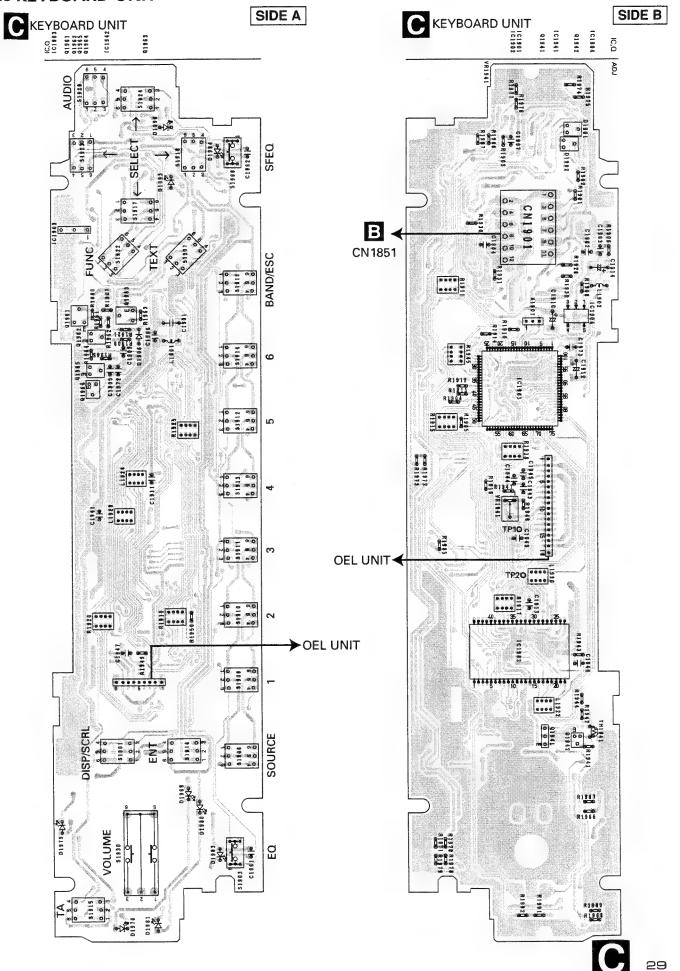
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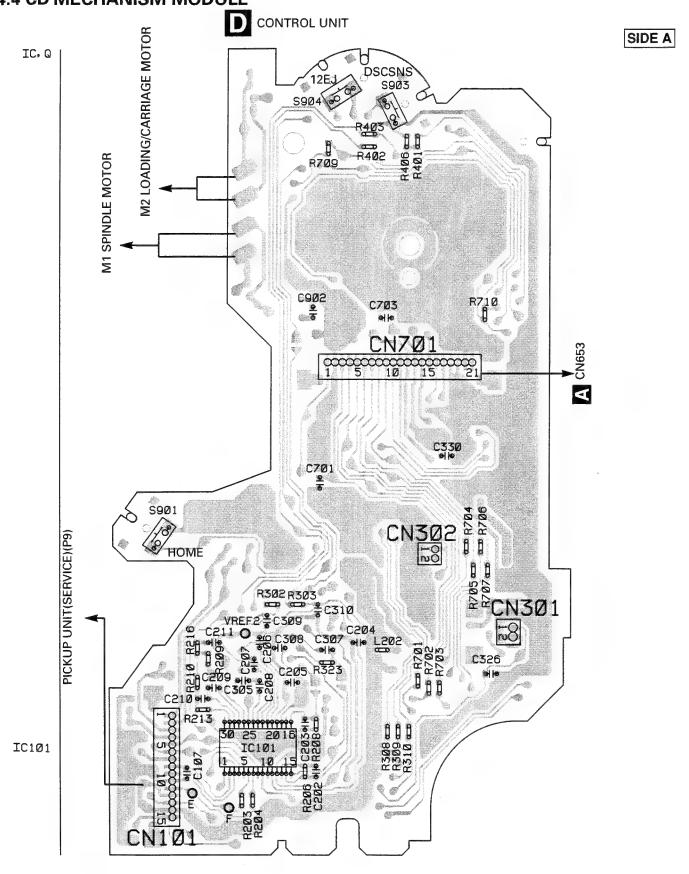
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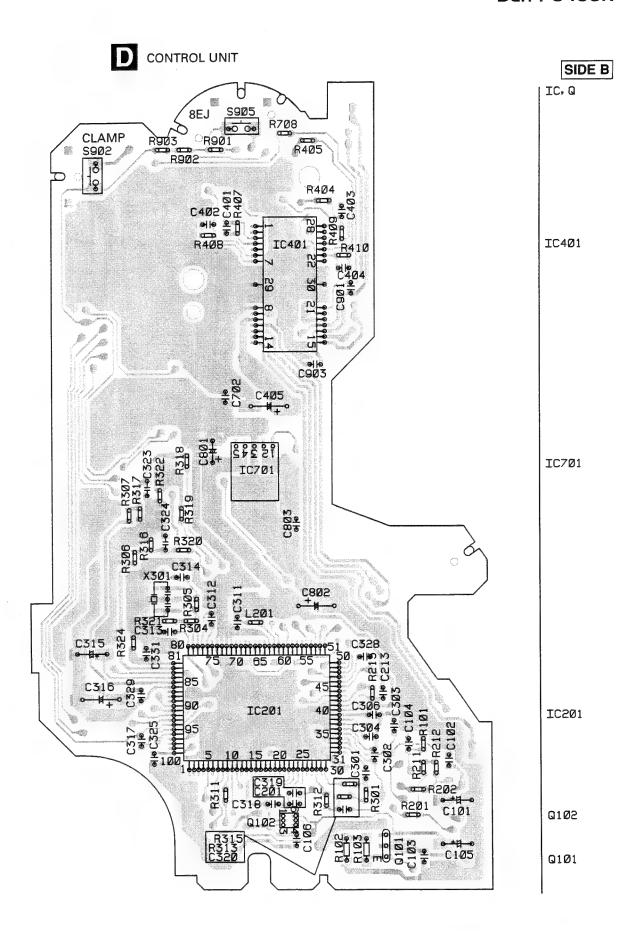
## **4.3 KEYBOARD UNIT**





D

**—** . 4



2

D

## 5. ELECTRICAL PARTS LIST

### NOTES:

- Parts whose parts numbers are omitted are subject to being not supplied.
- The part numbers shown below indicate chip components.

Chip Resistor

RS1/OSOOJ,RS1/OOSOOJ

Chip Capacitor (except for CQS.....)

CKS....., CCS....., CSZS.....

====Circuit Symbol and No.===Part Name	Part No.	=====Circuit Symbol and No.===Part Name	Part No.
Unit Number : CWM7984 Unit Name : Tuner Amp Unit Name : Tuner Amp Unit Name		D 807 Diode D 808 Diode D 851 Diode D 852 Diode D 901 Diode	DAP202U HZS11L(A1) HZS9L(C3) RB411D 1SR139-400
IC 101 IC IC 132 IC IC 203 IC IC 301 IC IC 501 IC	HA12187FP NJM4558MD PML008A PAL007A PM4009A	D 902 Diode D 903 Diode D 904 Diode D 911 Diode D 912 Diode	1SR139-400 1SR139-400 1SR139-400 1SR139-400 HZS6L(B1)
IC 601 IC IC 631 IC IC 801 IC IC 851 IC IC 961 IC	PD5700B PDH0045A TC7SET08FU NJM2360M S-80735ANDZI	D 921 Diode D 931 Diode D 932 Diode D 951 Diode D 981 Diode	HZS9L(C1) HZS7L(A1) HZS7L(C3) DAN202U DAN202U
Q 101 Transistor Q 102 Transistor Q 151 Transistor Q 152 Transistor Q 153 Transistor	2SA1037K DTC124EK 2SD1757K 2SD1757K IMH3A	D 982 Diode D 991 Diode ZNR 451 Surge Protector L 171 Inductor L 401 Ferri-Inductor	HZS9L(A2) HZS9L(B1) DSP-201M-A21F CTF1530 LAU2R2K
Q 155 Transistor Q 301 Transistor Q 351 Transistor Q 353 Transistor Q 401 Transistor	2SC2412K DTC124EK IMH3A IMH3A 2SC2412K	L 402 Ferri-Inductor L 403 Inductor L 451 Inductor L 501 Ferri-Inductor L 502 Ferri-Inductor	LAU4R7K LAU1R0K CTF1378 LAU101K LAU2R2K
O 402 Transistor O 501 Transistor O 801 Transistor O 802 Transistor O 803 Transistor	2SC2412K DTA124EK IMD2A 2SD1760F5 2SD1767	L 503 Inductor L 601 Ferri-Inductor L 801 Inductor L 802 Ferri-Inductor L 852 Inductor	CTF1378 LAU2R2K LAU100K LAU2R2K CTF1510
Q 804 Transistor Q 805 Transistor Q 851 Transistor Q 852 Transistor Q 911 Transistor	IMD2A DTC143EK 2SD1760F5 IMD2A 2SD1760F5	L 853 Inductor L 901 Choke Coil 600μH L 951 Ferri-Inductor X 501 Crystal Resonator 3.648MHz X 601 Radiator 10.00MHz	CTF1489 CTH1221 LAU2R2K CSS1447 CSS1475
O 913 Transistor O 921 Transistor O 922 Transistor O 923 Transistor O 931 Transistor	IMD2A 2SD2396 DTC114EK 2SB1238 IMX1	S 801 Switch(DSENS) FU 451 Fuse 200mA FM/AM Tuner Unit BZ 641 Buzzer	CSN1039 CEK1189 CWE1562 CPV1050
O 932 Transistor O 951 Transistor O 981 Transistor O 982 Transistor O 991 Transistor	2SC2412K 2SA1037K 2SC2412K IMD2A IMD2A	RESISTORS  R 101 R 102 R 103 R 104	RS1/16S101J RS1/16S470J RS1/16S101J RS1/16S222J
Q 999 Transistor D 131 Diode Network D 132 Diode Network D 133 Diode D 134 Diode	2SD2396 DA204U DA204U DAN202U DAP202U	R 105  R 106 R 107 R 108 R 109	RS1/16S103J RS1/16S562J RS1/16S332J RS1/16S150J RS1/16S181J
D 801 Diode D 803 Diode Network D 804 Diode D 805 Diode D 806 Diode	HZS6L(B1) DA204U DAN202U DAP202U DAN202U	R 110 R 111 R 112 R 113 R 114 R 143	RS1/16S181J RS1/16S223J RS1/16S223J RS1/16S102J RS1/16S102J RS1/16S104J

-==	==Circuit Symbol and No.===Part Name	Part No.	====Circuit	Symbol and No.===Part Name	Part No.
R R R R	144 145 146 147 148	RS1/16S104J RS1/16S563J RS1/16S563J RS1/16S474J RS1/16S474J	R 422 R 424 R 426 R 427 R 428		RS1/16S681J RS1/16S393J RS1/16S153J RS1/16S474J RS1/16S681J
R R R R	153 154 155 156 157	RS1/16S224J RS1/16S224J RS1/16S222J RS1/16S222J RS1/16S223J	R 452 R 453 R 501 R 503 R 506		RS1/16S102J RS1/16S0R0J RAB4C102J RS1/16S0R0J RS1/16S0R0J
R R R R	158 159 160 161 162	RS1/16S223J RS1/16S224J RS1/16S473J RS1/16S162J RS1/16S162J	R 511 R 513 R 518 R 551 R 553		RS1/16S102J RS1/16S225J RS1/16S681J RS1/16S0R0J RS1/16S0R0J
R R R R	163 164 165 166 171	RS1/16S272J RS1/16S272J RS1/16S104J RS1/16S104J RS1/16S0R0J	R 606 R 607 R 608 R 613 R 614		RS1/16S0R0J RS1/16S0R0J RS1/16S104J RS1/16S222J RS1/16S222J
R R R R	172 230 241 242 247	RS1/16S0R0J RS1/16S0R0J RS1/16S0R0J RS1/16S0R0J RS1/16S101J	R 615 R 616 R 617 R 618 R 619		RS1/16S104J RS1/16S473J RS1/16S0R0J RS1/16S222J RS1/16S473J
R R R R	248 249 250 287 288	RS1/16S101J RS1/16S101J RS1/16S101J RS1/16S0R0J RS1/16S0R0J	R 620 R 621 R 622 R 623 R 624		RS1/16S472J RS1/16S473J RS1/16S104J RS1/16S473J RS1/16S0R0J
R R R R	289 290 301 302 304	RS1/16S0R0J RS1/16S0R0J RS1/16S103J RS1/16S103J RS1/16S331J	R 626 R 630 R 631 R 632 R 633		RS1/16S104J RS1/16S104J RS1/16S102J RS1/16S104J RS1/16S104J
R R R R	351 352 355 356 357	RS1/16S821J RS1/16S821J RS1/16S821J RS1/16S821J RS1/16S223J	R 634 R 638 R 640 R 641 R 642		RS1/16S473J RS1/16S0R0J RS1/16S0R0J RS1/16S102J RS1/16S0R0J
R R R R	358 361 362 363 368	RS1/16S223J RS1/16S223J RS1/16S223J RS1/16S0R0J RS1/16S0R0J	R 643 R 650 R 651 R 653 R 801		RS1/16S0R0J RS1/16S222J RS1/16S222J RS1/16S0R0J RS1/16S332J
R R R R	401 402 403 404 405	RS1/16S473J RS1/16S473J RS1/16S681J RS1/16S681J RS1/16S681J	R 804 R 805 R 806 R 807 R 808		RS1/16S1R0J RS1/16S221J RS1/16S221J RS1/16S473J RS1/16S473J
R R R R	406 407 409 410 411	RS1/16S102J RS1/16S473J RS1/16S681J RS1/16S103J RS1/16S681J	R 809 R 810 R 811 R 812 R 813		RS1/16S102J RS1/16S222J RS1/16S222J RS1/16S222J RS1/16S222J
R R R R	412 413 414 415 416	RS1/16S681J RS1/16S681J RS1/16S473J RS1/16S472J RS1/16S473J	R 814 R 815 R 816 R 817 R 819		RS1/16S222J RS1/16S473J RS1/16S104J RD1/4PU391J RS1/16S222J
R R R R	417 418 419 420 421	RS1/16S473J RS1/16S473J RS1/16S222J RS1/16S222J RS1/16S681J	R 820 R 851 R 852 R 853 R 854		RS1/16S222J RS1/16S331J RD1/4PU302J RD1/4PU302J RS1/16S121J

==:	===Circuit Symbol and No.===Part Name	Part No.	===	====Circuit Symbol and No.===Part Name	Part No.
R R R R	855 856 857 912 913	RS1/16S391J RS1/16S1R0J RS1/16S331J RS1/16S152J RS1/16S223J	00000	210 211 212 213 214	CEJQ1R0M50 CEJQNP4R7M16 CEJQNP4R7M16 CEJQNP4R7M16 CEJQNP4R7M16
R R R	914 921 922 923	RS1/16S152J RS1/10S1R0J RD1/4PU221J RS1/16S103J	00000	215 216 306 307 4700µF/16V 309	CEJQNP4R7M16 CEJQNP4R7M16 CEHAR330M10 CCH1367 CKSRYB104K16
R R R R	924 925 931 932	RD1/4PU222J RS1/16S122J RS1/16S472J RS1/16S473J RS1/16S103J	00000	310 311 313 314 315	CEHAR100M16 CKSQYB225K10 CKSRYB474K10 CKSRYB474K10 CKSRYB474K10
R R R	934 935 936 937	RS1/16S473J RS1/16S104J RS1/16S103J RS1/16S473J	0000	316 321 322 323	CKSRYB474K10 CKSRYB474K10 CKSRYB474K10 CKSRYB474K10 CKSRYB474K10
R R R R R	938 939 951 952 953	RD1/4PU102J RD1/4PU102J RD1/4PU153J RS1/16S472J RS1/16S472J	0 00000	324 325 351 352 355	CKSRYB474K10  CKSQYB225K10  CEJQ100M16  CEJQ100M16  CEJQ100M16
R R R R R	954 962 964 982 983	RS1/16S102J RS1/16S102J RS1/16S822J RS1/16S223J RS1/16S223J	0 00000	356 401 403 404 405	CEJQ100M16  CKSRYB182K50  CKSRYB473K25  CEJQ101M6R3  CKSRYB103K50
R R R R	984 985 991 992 993	RS1/16S473J RD1/4PU102J RD1/4PU221J RD1/4PU221J RS1/16S472J	00000	406 408 409 411 413	CEJQ220M10  CKSRYB223K50  CKSRYB223K50  CKSRYB472K50  CKSRYB472K50
R R R	994 995 997 PACITORS	RS1/16S222J RS1/16S0R0J RAB4C102J	0000	425 440 453 501	CKSRYB102K50 CKSRYB103K50 CKSRYB224K16 CEJQ220M6R3
00000	101 102 139 140 141	CKSRYB104K16 CKSRYB104K16 CKSRYB104K16 CKSRYB104K16 CKSRYB104K16	0000	502 503 504 505 506	CKSRYB104K16 CCSRCH270J50 CCSRCH270J50 CKSRYB104K16 CKSRYB471K50
00000	142 143 144 145	CKSRYB104K16 CKSRYB474K10 CKSRYB474K10 CCSRCH101J50 CCSRCH101J50	C	507 508 509 510 511	CKSRYB471K50 CKSRYB104K16 CEJQ220M6R3 CCSRCH101J50
0000	146 147 151 152 153	CKSRYB103K50 CEJQ1R0M50 CEJQ1R0M50 CKSRYB223K25	000 00	601 602 603 604	CKSRYB102K50 CEJQ4R7M35 CKSRYB102K50 CKSRYB472K50 CCSRCH180J50
000	161 162 171	CKSRYB123K25 CKSRYB123K25 CEJQ470M10 CKSRYB104K16	CCC	605 801 802	CCSRCH220J50 CKSRYB103K50 CEJQ470M10
c C	172 173 177	CEJQ100M16 CCSRCH100D50 CCSRCH100D50	00000	803 805 806 812 851	CKSRYB104K16 CKSRYB103K25 CKSRYB473K25 CKSYB105K25 CEJQ470M16
0000	179 180 191 192	CCSRCH100D50 CCSRCH100D50 CEJQ1R0M50 CEJQ1R0M50	0000	853 4.7μF 855 856 857	CCG1111 CEJQ100M25 CCSRCH331J50 CEJQ330M25
00000	205 206 207 208 209	CEJQR22M50 CEJQR22M50 CEJQ1R0M50 CEJQ1R0M50 CEJQ1R0M50	С	858	CKSRYB104K16

====Circuit Symbol and No.===Part Name	Part No.	====Circuit Symbol and No.===Part Name	Part No.
C 859 C 860 C 861 C 912 C 913	CEJQ101M10 CKSRYB104K16 CKSRYB103K50 CKSRYB472K50 CKSRYB103K50	VR 1941 Semi-fixed 20kΩ(B) OEL Unit RESISTORS	CCP1231 MXS8017
C 914 C 921 330μF/10V C 922 C 923 C 931	CEJQ470M10 CCH1181 CKSRYB103K50 CEJQ101M16 CEJQ100M16	R 1901 R 1902 R 1903 R 1904 R 1905	RS1/16S222J RS1/16S222J RS1/16S473J RS1/16S103J RS1/16S682J
C 932 C 963 C 991 C 992 C 993	CKSRYB104K16 CEJ02R2M50 CKSRYB473K25 CKSRYB102K50 CEJQ101M10	R 1906 R 1907 R 1908 R 1909 R 1910	RS1/16S121J RS1/16S2R2J RS1/16S222J RS1/16S154J RS1/16S473J
C 999 470µF/16V  Unit Number : CWM7990	CCH1183	R 1911 R 1912 R 1913 R 1914 R 1915	RAB4C101J RS1/16S473J RS1/16S473J RS1/16S103J RAB4C101J
Unit Name : Keyboard Un MISCELLANEOUS	it	R 1916	RS1/16S101J
IC 1901 IC IC 1902 IC IC 1903 IC	PD5706A PD8088A TSOP1840SB3V	R 1917 R 1918 R 1919 R 1920	RAB4C101J RAB4C101J RAB4C101J RAB4C101J
IC 1904 IC Q 1941 Transistor Q 1942 Transistor	S-818A33AUC-BGN 2SD1664 2SC4617	R 1925 R 1926 R 1931 R 1941	RAB4C101J RS1/16S101J RS1/16S0R0J RS1/16S333J
D 1901 Diode D 1902 Diode D 1906 Diode D 1979 LED	DAP202U DAN202U 1SS355 CL170PGCD	R 1942 R 1943 R 1944	RS1/16S683J RS1/16S392J RS1/16S393J
D 1980 LED D 1981 LED D 1983 LED	CL170PGCD CL170PGCD CL170PGCD	R 1945 R 1946 R 1947	RAB4C102J RS1/16S222J RS1/16S103J
D 1984 LED D 1993 LED D 1996 LED	CL170PGCD CL170PGCD CL170PGCD	R 1949 R 1950 R 1966 R 1967	RS1/16S102J RS1/16S102J RS1/16S101J RS1/16S101J
L 1901 Inductor L 1902 Inductor L 1922 Inductor-Array L 1923 Inductor-Array	CTF1530 CTF1530 CTF1421 CTF1421	R 1968 R 1969	RS1/16S101J RS1/16S101J RS1/16S270J
L 1924 Inductor-Array L 1990 Inductor-Array TH 1941 Thermistor	CTF1421 CTF1421 CCX1037	R 1970 R 1971 R 1972 R 1973	RS1/16S270J RS1/16S270J RS1/16S270J RS1/16S270J
X 1901 Radiator 10.0MHz S 1901 Switch	CSS1577 CSG1107	R 1974 R 1975	RS1/16S270J RS1/16S270J
S 1903 Push Switch S 1906 Switch S 1907 Switch S 1908 Push Switch	CSG1111 CSG1107 CSG1107	R 1976 R 1977 R 1978	RS1/16S101J RS1/16S101J RS1/16S181J
S 1908 Push Switch S 1909 Switch	CSG1111 CSG1107	R 1979 R 1980 R 1985	RS1/16S181J RS1/16S0R0J RS1/16S0R0J
S 1910 Switch S 1911 Switch S 1912 Switch S 1913 Switch S 1914 Switch	CSG1107 CSG1107 CSG1107 CSG1107 CSG1107	R 1986 R 1987 CAPACITORS	RS1/16S181J RS1/16S181J
S 1915 Switch S 1916 Switch S 1917 Switch S 1918 Switch S 1919 Switch	CSG1107 CSG1107 CSG1107 CSG1107 CSG1107	C 1901 C 1902 C 1903 C 1905 C 1909	CKSYB105K25 CKSRYB104K16 CKSRYB474K10 CKSRYB103K25 CKSRYB473K16
S 1920 Switch S 1922 Switch S 1923 Switch S 1924 Switch S 1930 Encoder(VOLUME)	CSG1107 CSG1107 CSG1107 CSG1107 CSD1059	C 1910 C 1911 C 1912 C 1913 C 1914	CSZSR4R7M10 CKSRYB103K25 CSZSR4R7M10 CKSRYB103K50 CSZSR4R7M10

===	==Circu	uit Symbol and No.===Part Name	Part No.	==	===Circuit Symbol and No.===Part Name	Part No.
000	1941 1942 1943 1944 1945		CKSRYB104K25 CKSRYB104K16 CKSRYB104K25 CKSRYB104K25 CKSRYB104K25 CKSRYB104K25	R R R R	403 404 405 407 408	RS1/16S103J RS1/16S183J RS1/16S123J RS1/16S622J RS1/16S622J
CC	1946 1947 1972 Un	it Number:CWM7986	CKSRYB104K16 CKSRYB104K25 CKSRYB103K25	R R R R	409 410 701 702 703	RS1/16S113J RS1/16S752J RS1/16S102J RS1/16S221J RS1/16S221J
S R R	1850 1850 1852 1853	it Name : Panel Unit LED Push Switch(EJECT)	CL220PGC CSG1112 RS1/16S101J RS1/16S101J	R R R R	704 705 706 707 708	RS1/16S221J RS1/16S221J RS1/16S221J RS1/16S221J RS1/16S102J
D	Un	it Number: CWX2481 it Name: Control Unit ANEOUS	CKSRYB223K50	R R R R	709 710 901 902 903	RS1/16S102J RS1/16S102J RS1/16S104J RS1/16S473J RS1/16S273J
				CA	PACITORS	
0 10 10 10	101 201 401 701 101	IC IC IC IC Transistor	TA2153FN TC9495F2 BA5996FM BA05SFP 2SD1664	00000	101 102 103 104 105	CEV470M6R3 CKSRYB102K50 CKSRYB104K16 CKSRYB224K16 CEV470M6R3
O L L X S	102 201 202 301 901	Transistor Inductor Inductor Ceramic Resonator 16.934MHz Spring Switch(HOME)	UMD2N CTF1546 CTF1546 CSS1525 CSN1051	00000	106 107 201 202 204	CKSRYB104K16 CKSRYB105K6R3 CKSRYB104K16 CCSRCH560J50 CKSRYB224K16
SSSS	902 903 904 905	Spring Switch(CLAMP) Spring Switch(DSCSNS) Spring Switch(12EJ) Spring Switch(8EJ)	CSN1052 CSN1051 CSN1052 CSN1051	00000	205 206 207 208	CKSRYB224K16 CKSRYB273K25 CKSRYB273K25 CKSRYB104K16
RES	SISTO	RS		С	209	CKSRYB104K16
R R R R	101 102 103 201 202		RS1/16S222J RS1/8S120J RS1/8S100J RS1/16S513J RS1/16S513J	00000	210 211 301 302 303	CCSRCK2R0C50 CCSRCH220J50 CKSRYB153K25 CKSRYB104K16 CKSRYB103K50
R R R R	203 204 206 208 209		RS1/16S823J RS1/16S823J RS1/16S823J RS1/16S124J RS1/16S183J	00000	304 305 306 307 308	CKSRYB103K50 CKSRYB104K16 CKSRYB104K16 CKSRYB333K16 CKSRYB104K16
R R R R	210 211 212 213 215		RS1/16S153J RS1/16S103J RS1/16S103J RS1/16S124J RS1/16S0R0J	CCCCC	309 310 311 312 315	CKSRYB473K16 CKSRYB473K16 CKSRYB104K16 CKSRYB104K16 CEV220M6R3
R R R R	216 301 302 303 304		RS1/16S471J RS1/16S333J RS1/16S332J RS1/16S332J RS1/16S514J	00000	317 318 319 320 325	CKSRYB104K16 CKSRYB104K16 CKSRYB104K16 CCSRCH470J50 CKSRYB471K50
R R R R	306 307 312 313 315		RS1/16S102J RS1/16S102J RS1/16S103J RS1/16S473J RS1/16S334J	00000	328 329 330 331 401	CKSRYB472K50 CKSRYB104K16 CKSRYB104K16 CKSRYB104K16 CKSRYB221K50
R R R R	321 322 323 401 402		RS1/16S331J RS1/16S0R0J RS1/16S332J RS1/16S684J RS1/16S103J	00000	402 403 404 405 702	CKSRYB221K50 CKSRYB153K25 CKSRYB103K50 CEV101M10 CKSRYB104K16

===:	==Circu	it Symbol and No.===Part Name	Part No.
CCCC	703 801 802 803	10μF/10V	CKSRYB104K16 CCH1349 CEV101M10 CKSRYB224K16
Mis	cellan	eous Parts List	
M M	1 2	Pickup Unit(Service)(P9) Motor Unit(SPINDLE) Motor Unit(LOADING/CARRIAGE)	CXX1480 CXB6007 CXB5903

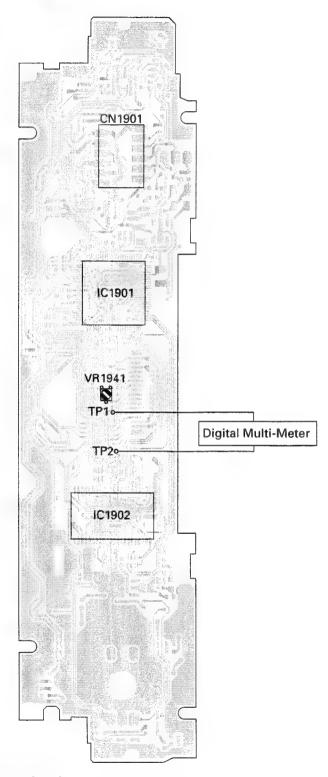
### 6. ADJUSTMENT

# 6.1 OEL UNIT ADJUSTMENT



Adjustment point

KEYBOARD UNIT (SIDE B)



<When the OEL Unit has been replaced>

1. Use VR1941 to adjust the resistance between TP1 and TP2 to  $6.60k\Omega$ .

### **6.2 CD ADJUSTMENT**

### 1) Precautions

This unit uses a single power supply (+5V) for the regulator. The signal reference potential, therefore, is connected to VREF(approx. 2.1V) instead of GND.

If VREF and GND are connected to each other by mistake during adjustments, not only will it be impossible to measure the potential correctly, but the servo will malfunction and a severe shock will be applied to the pick-up. To avoid this, take special note of the following.

Do not connect the negative probe of the measuring equipment to VREF and GND together. It is especially important not to connect the channel 1 negative probe of the oscilloscope to VREF with the channel 2 negative probe connected to GND.

Since the frame of the measuring instrument is usually at the same potential as the negative probe, change the frame of the measuring instrument to floating status.

If by accident VREF comes in contact with GND, immediately switch the regulator or power OFF.

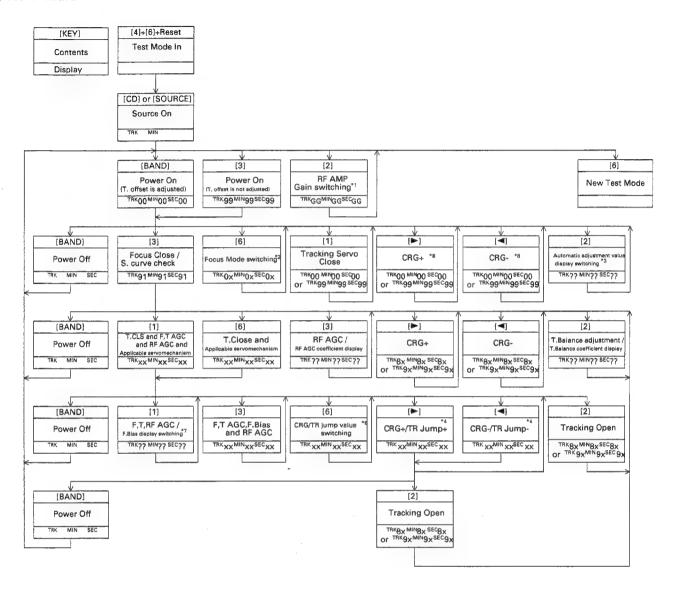
- Always make sure the regulator is OFF when connecting and disconnecting the various filters and wiring required for measurements.
- Before proceeding to further adjustments and measurements after switching regulator ON, let the player run for about one minute to allow the circuits to stabilize.
- Since the protective systems in the unit's software are rendered inoperative in test mode, be very careful to avoid mechanical and /or electrical shocks to the system when making adjustment.
- The RFI and RFO signals are easy to oscillate because of a wide band. When observing them, insert a resistor of about 1 k $\Omega$  to the series.
- This equipment will not guarantee the load ejection operation when the mechanical unit is turned upside down. In particular, if the ejection operation is incorrectly performed and recovery is disabled, the recovery is enabled by resetting a product or turning ACC off to on.

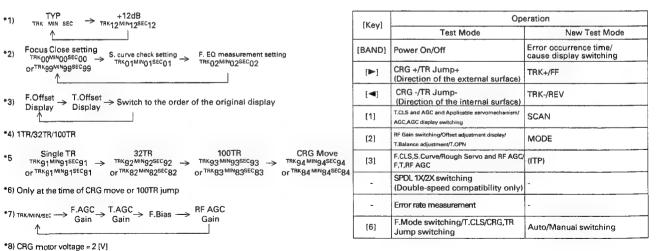
### 2) Test Mode

This mode is used for adjusting the CD mechanism module of the device.

- Test mode starting procedure
   Reset while pressing the 4 and 6 keys together.
- Test mode cancellation Switch ACC, back-up OFF.
- After pressing the EJECT key, do not press any other key until the disk is completely ejected.
- If the ➤ or <</li>
   key is pressed while focus search is in progress, immediately turn the power off (otherwise the actuator may be damaged due to adhesion of the lenses).
- Jump operation of TRs other than 100TR continues after releasing the key. CRG move and 100TR jump operations are brought into the "Tracking close" status when the key is released.
- Powering Off/On resets the jump mode to "Single TR(91)", the RF AMP gain setting to 0 dB, and the automatic adjustment value to the initial value.

### Flow Chart





### 6.3 CHECKING THE GRATING AFTER CHANGING THE PICKUP UNIT



### · Note:

The grating angle of the PU unit cannot be adjusted after the PU unit is changed. The PU unit in the CD mechanism module is adjusted on the production line to match the CD mechanism module and is thus the best adjusted PU unit for the CD mechanism module. Changing the PU unit is thus best considered as a last resort. However, if the PU unit must be changed, the grating should be checked using the procedure below.

### · Purpose :

To check that the grating is within an acceptable range when the PU unit is changed.

### · Symptoms of Mal-adjustment:

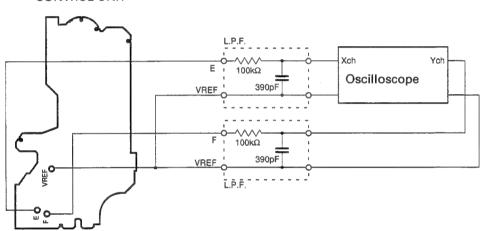
If the grating is off by a large amount symptoms such as being unable to close tracking, being unable to perform track search operations, or taking a long time for track searching.

### · Method:

- Measuring Equipment
- Measuring Points
- Disc Mode

- · Oscilloscope, Two L.P.F.
- · E, F, VREF
- ABEX TCD-784
- TEST MODE

### CONTROL UNIT



### Checking Procedure

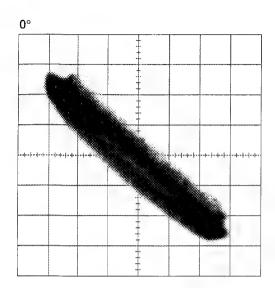
- 1. In test mode, load the disc and switch the 5V regulator on.
- 2. Using the ▶ and ◀ buttons, move the PU unit to the innermost track.
- 3. Press key 3 to close focus, the display should read "91". Press key 2 to implement the tracking balance adjustment the display should now read "81". Press key 3. The display will change, returning to "81" on the fourth
- 4. As shown in the diagram above, monitor the LPF outputs using the oscilloscope and check that the phase difference is within 75°. Refer to the photographs supplied to determine the phase angle.
- 5. If the phase difference is determined to be greater than 75° try changing the PU unit to see if there is any improvement, If, after trying this a number of times, the grating angle does not become less than 75° then the mechanism should be judged to be at fault.

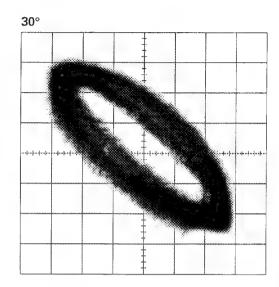
### Note

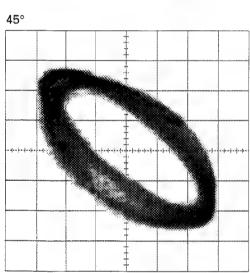
Because of eccentricity in the disc and a slight misalignment of the clamping center the grating waveform may be seen to "wobble" (the phase difference changes as the disc rotates). The angle specified above indicates the average angle.

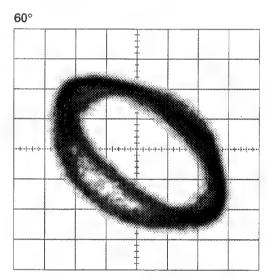
Reloading the disc changes the clamp position and may decrease the "wobble".

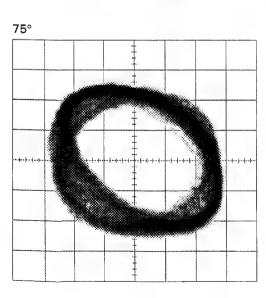
### **Grating waveform**

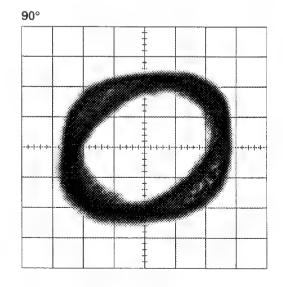












### **6.4 CD TEST MODE**

### Error Messages

If a CD is not operative or stopped during operation due to an error, the error mode is turned on and cause(s) of the error is indicated with a corresponding number. This arrangement is intended at reducing nonsense calls from the users and also for facilitating trouble analysis and repair work in servicing.

### (1) Basic Indication Method

1) When SERRORM is selected for the CSMOD (CD mode area for the system), error codes are written to DMIN (minutes display area) and DSEC (seconds display area). The same data is written to DMIN and DSEC. DTNO remains in blank as before.

### 2) Head unit display examples

Depending on display capability of LCD used, display will vary as shown below. xx contains the error number.

8-digit display	6-digit display	4-digit display
ERROR-xx	ERR-xx	E-xx

(2) Error Code List

(2) Errc	or Code List		
Code	Class	Displayed error code	Description of the code and potential cause(s)
10	Electricity	Carriage Home NG	CRG can't be moved to inner diameter.
		SERVO LSI Com-	CRG can't be moved from inner diameter.
		munication Error	ightarrow Failure on home switch or CRG move mechanism.
			Communication error between microcomputer and SERVO LSI.
11	Electricity	Focus Servo NG	Focusing not available.
			ightarrow Stains on rear side of disc or excessive vibrations on REWRITABLE.
12	Electricity	Spindle Lock NG	Spindle not locked. Sub-code is strange (not readable).
		Subcode NG	ightarrow Failure on spindle, stains or damages on disc, or excessive vibrations.
			A disc not containing CD-R data is found.
			Turned over disc are found, though rarely.
			CD signal error.
17	Electricity	Setup NG	AGC protection doesn't work. Focus can be easily lost.
			ightarrow Damages or stains on disc, or excessive vibrations on REWRITABLE.
30	Electricity	Search Time Out	Failed to reach target address.
			→ CRG tracking error or damages on disc.
44	Electricity	ALL Skip	Skip setting for all track.
			(CD-R/RW)
50	Mechanism	CD On Mech Error	Mechanical error during CD ON.
			→ Defective loading motor, mechanical lock and mechanical sensor.
A0	System	Power Supply NG	Power (VD) is ground faulted.
			→ Failure on SW transistor or power supply (failure on connector).

Remarks: Mechanical errors are not displayed (because a CD is turned off in these errors).

Unreadable TOC does not constitute an error. An intended operation continues in this case.

Upper digits of an error code are subdivided as shown below:

1x: Setup relevant errors, 3x: Search relevant errors, Ax: Other errors.

### New Test Mode

S-CD plays the same way as before.

If an error such as off focus, spindle unlocking, unreadable sub-code, or sound skipping occurs after setup, its cause and time occurred (in absolute time) are displayed.

During setup, operational status of the control software is displayed.

These displays and functions are prepared for enhancing aging in the servicing and efficiency of trouble analysis.

- (1) Shifting to the New Test Mode
- 1) Turn on the current test mode by starting the reset from the key.
- ② Select S-CD for the source through the specified procedure including use of the [SOURCE] key, and inserting the disc. Then, press the [Jump Mode Selector] key while maintaining the regulator turned off.
- 3 After the above operations, the new test mode remains on irrespective of whether the S-CD is turned on or off.
  You can reset the new test mode by turning on the reset start.

(2) Key Correspondence

Key		Test mode		New test mode	
	Regulator Off	Regulator On	In-play	Error Production	
BAND	To regulator on	To regulator off	-	Time/Err.No. switching	
<b>&gt;</b>	_	FWD-Kick	FF/TR+	_	
<b>◄</b>	_	REV-Kick	REV/TR-	_	
1	_	Tracking Close	Scan	_	
2	_	Tracking Open	Mode	_	
3	_	Focus Close	-	_	
-	_	Focus Open	_	_	
-	_	Jump Off	-	_	
6	To new test mode	Jump mode switching	Auto/Manu	_	

Note: Eject and CD on/off is performed in the same procedure as that for the normal mode.

(3) Cause of Error and Error Code

Code	Class	Contents	Description and cause
40	Electricity	Off focus detected.	FOK goes low.
			→ Damages/stains on disc, vibrations or failure on servo.
41	Electricity	Spindle unlocked.	LOCK = Low continued for 150 msec.
			→ Damages/stains on disc, vibrations or failure on servo.
42	Electricity	Sub-code unreadable.	Sub-code was unreadable for 500 msec.
			ightarrow Damages/stains on disc, vibrations or failure on servo.
43	Electricity	Sound skipping detected.	Last address memory function was activated.
			→ Damages/stains on disc, vibrations or failure on servo.

Note: Mechanical errors during aging are not displayed.

(4) Display of Operational Status during Setup

Status No.	Contents	Protective action
21	Focus search start	Focus search timeout.
22	Focus search 2	Focus search timeout.
23	Focus search 3	Focus search timeout.
24	Focus search 4	Focus search timeout.
25	Focus search(Setup protection)	Focus slips off.
26	Focus search(Fast recovery)	Focus slips off.
27	RF detection	Focus slips off.
28	Spindle rough servocontrol	Focus slips off.
29	Tracking balance adjustment start	Focus slips off.
30	Tracking balance adjustment 2	Focus slips off.
31	Tracking balance adjustment 3	Focus slips off.
32	Tracking close start(Spindle stationary servocontrol setting)	Focus slips off.
33	Tracking close 2	Focus slips off.
34	Tracking close 3	Focus slips off.
35	Focus/Tracking AGC start	Focus slips off.
36	Focus/Tracking AGC 2	Focus slips off.
37	Focus/Tracking AGC 3	Focus slips off.
38	Focus/Tracking AGC 4	Focus slips off.
39	Focus/Tracking AGC 5	Focus slips off.
40	Focus/Tracking AGC 6	Focus slips off.
41	Focus/Tracking AGC 7	Focus slips off.
42	Focus/Tracking AGC 8	Focus slips off.
43	FE bias start	Focus slips off.
44	FE bias 2	Focus slips off.
45	RF AGC start	Focus slips off.
46	RF AGC 2	Focus slips off.
47	Lock check start	Focus slips off.
48	Lock is being checked	Focus slips off.
49	Subcode check start	Focus slips off, spindle lock is not performed.  Focus slips off, no subcode can be read.

(5) Display Examples

1) During Setup

8-digit display, 6-digit display

4-digit display(Auto setting)

4-digit display(Manual setting)

TNO. Min Sec 11 11' 11" TNO. 11 Min Sec 11' 11"

2) During Operation (TOC read, TRK search, Play, FF and REV)

The same as in the normal mode.

3) When a Protection Error Occurred

(A) Error display ((A) $\leftarrow\rightarrow$ (B), (C) : BAND key)

8-digit display

6-digit display

4-digit display

ERROR-xx

ERR-xx

E-xx

(B) Error occurrence timing display in track no. ((B) $\leftarrow\rightarrow$ (C) : Auto/Manual key)

8-digit display, 6-digit display

4-digit display(Auto setting)

TNO. Min Sec

TNO.

10 40' 05"

10

(C) Error occurrence timing display in absolute time. ((B) $\leftarrow\rightarrow$ (C) : Auto/Manual key)

8-digit display, 6-digit display

4-digit display(Manual setting)

TNO. Min Sec

Min Sec

10 40' 05"

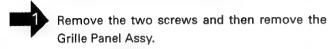
40' 05"

### 7. GENERAL INFORMATION

### 7.1 DIAGNOSIS

### 7.1.1 DISASSEMBLY

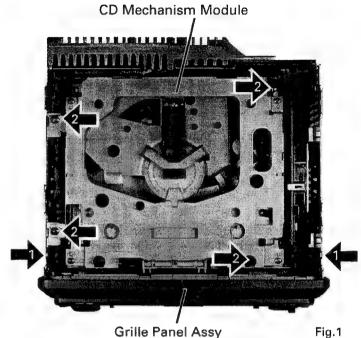
- Removing the Case (not shown)
- 1. Remove the Case.
- Removing the Grille Panel Assy (Fig.1)



■ Removing the CD Mechanism Module (Fig.1)



Remove the four screws and then remove the CD Mechanism Module.



Removing the Tuner Amp Unit (Fig.2)



Remove the two screws.



Remove the three screws.

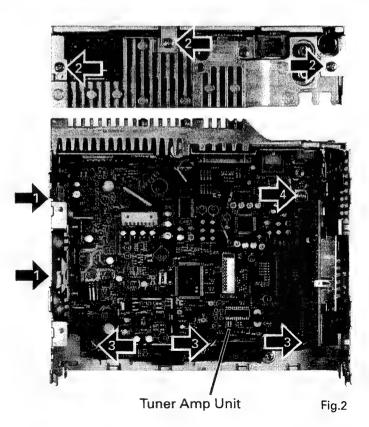


Straight the tabs at three locations indicated.



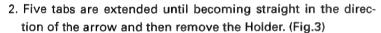
Remove the screw and then remove the Tuner Amp Unit.

\*) Tuner Amp Unit is different partially from this photo.



### Removing the OEL Unit

- Apply hot air to the cable pins for the anode terminal using a blower used for removing a flat-packaged IC or something like that. When all the pins are peeling off from the PCB, pinch the cable with a pair of tweezers and remove it slowly from the PCB. (Fig.3)
- \* Be careful not to remove other electrical parts when you use a blower. Especially, when hot air is appropriated to the VR1902 too much, the volume will destroy.
- \* Flexible cable may not remove easily by transforming the Bosses by the hot air of the Blower.



- Slowly set up the OEL Unit. At this time, the stress is prevented from hanging to flexible cable in the Cathode terminal. (Fig.4)
- 4. The Cathode terminal is removed according to the procedure same as the Anode terminal, and the OEL Unit is removed. (Fig.4)
- 5. Remove the Holder. (Remove after removing the Cathode terminal without fail.) (Fig.4)

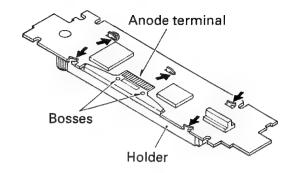
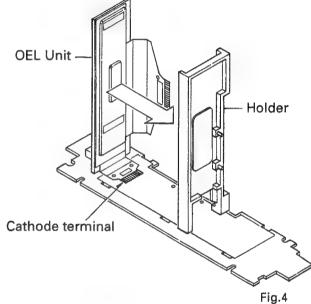
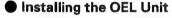
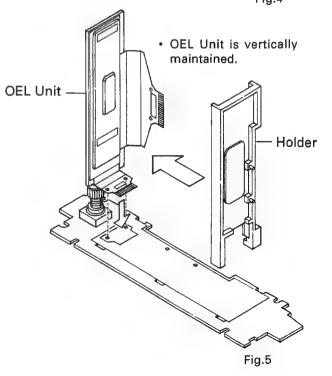


Fig.3



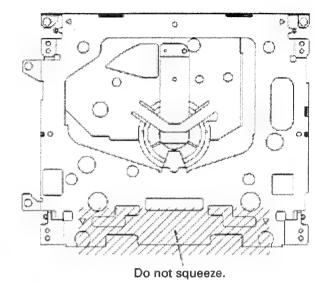


- 1. Install the Holder in the OEL Unit. (Fig.5)
- When soldering the flexible cable for the Cathode terminal on the PCB, use a pair of tweezers. First, insert the tips of tweezers into 2 holes in the flexible cable, then into the 2 holes in the PCB. (Fig.5)
- 3. Position the flexible cable on the PCB so that their lands touch each other. (Fig.5)
- 4. Apply solder to each pin of the flexible cable. (Fig.5)
- \* Appropriate soldering iron lightly so that the stress should not hang to Flexible cable.
- 5. Lay down the OEL Unit. (Fig.5)
- 6. Install the Holder. (Fig.3)
- 7. When soldering the flexible cable for the Anode terminal on the PCB, first, insert the Bosses on the PCB into the 2 holes in the flexible cable. Then, take the same procedures 2 and 3 as that for the Cathode terminal to solder the cable pins. (Fig.3)



### How to hold the Mechanical Unit

- 1. Hold the top and bottom frame.
- Do not squeeze top frame's front portion too tight, because it is fragile.



### How to remove the Top and Bottom Frame

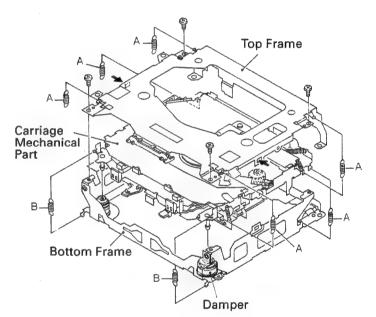
- When the disk is in "clamp" state, unlock Spring A (6 pieces) and Spring B (2 pieces), and unscrew screws (4 pieces).
- 2. Unlock each 1 of pawl at the both side of the frame, then remove the top frame.
- 3. Remove the Carriage Mechanical part in such way that; you remove the mechanical part from 3 pieces of Damper while slowly pulling up the part.
- 4. Now, the top frame has been removed, and under this state, fix the genuine Connector again, and eject the disk.

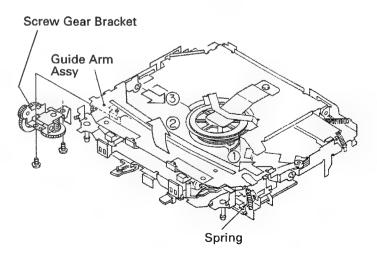
(Caution)

When you reassemble the Carriage Mechanical part, apply a bit of alcohol to Dampers.

### How to remove the Guide Arm Assy

- 1. Unlock the spring (1 piece) at the right side of the assembly.
- Unscrew screws (2 pieces), then remove the Screw Gear Bracket.
- 3. Shift the Guide Arm Assy to the left and slowly rotate it to the upper direction.
- 4. When the Guide Arm Assy rotates approximately 45 degree, shift the Assy to the right side direction and remove it.

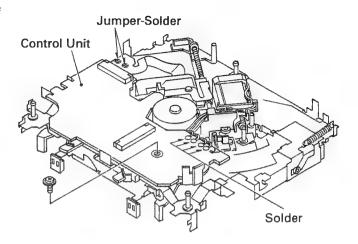




### How to remove the Control Unit

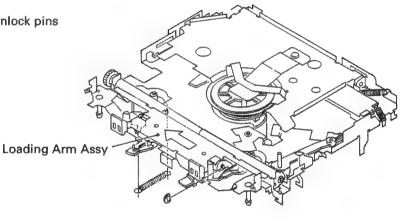
- Give jumper-solder treatment to the Flexible Wire of the Pickup unit, then remove the wire from the Connector.
- 2. Remove all 4 points of solder-treatment on the Lead Wire. Also, unscrew the screw(1 piece).
- 3. Then, Remove the Control unit. (Caution)

Be careful not to damage SW when you reassemble the Control Unit into the device.



### How to remove the Loading Arm Assy

- 1. Unlock the spring (1 piece) and remove the E ring (1 piece) of the Fulcrum Shaft.
- 2. Shift the arm to the left side direction and unlock pins (2 pieces).

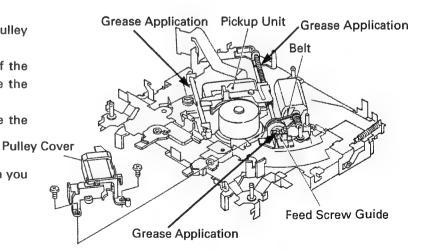


### How to remove the Pickup Unit

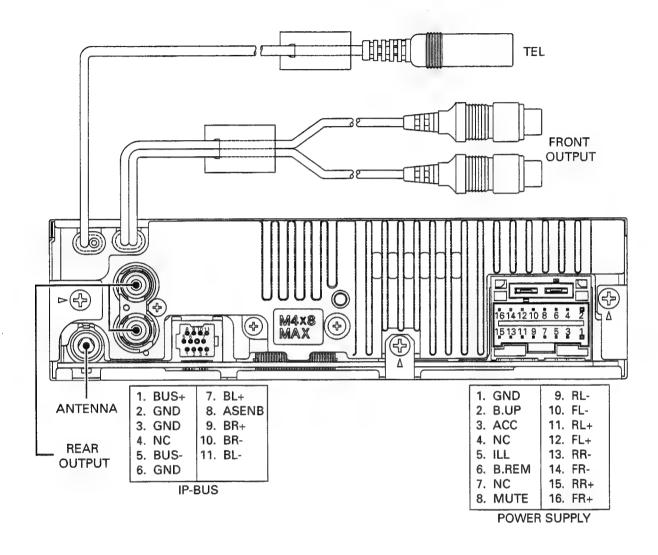
- Unscrew 2 pieces of screws, then remove the Pulley Cover.
- 2. Remove the Feed Screw unit from the pawl of the Feed Screw Guide (The pawl is located inside the guide).
- 3. Remove the belt from the Pulley, then remove the Pickup unit.

(Caution)

Make sure not to stain the belt with grease when you fix the belt.



### 7.1.2 CONNECTOR FUNCTION DESCRIPTION



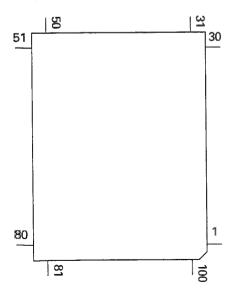
### 7.2 IC

● Pin Functions (PD5700A)

	ctions (PD570		
Pin No.	Pin Name	1/0	Function and Operation
1	TUNPDO	0	TUNER:Data output(PLL)
2	TUNPCK	0	TUNER:Clock output(PLL)
3	EMUTE	0	EVOL:Mute output (Not used)
4	VST	0	EVOL:Strobe output
5	VDT	0	EVOL:Data output
6	NC	-	Not used
7	VCK	0	EVOL:Clock output
8	BYTE	10	Vss
9		+	A CONTRACTOR OF THE CONTRACTOR
	CNVSS	<b>.</b>	Vss
10	TELIN		TEL:Telephone mute input
11	HTELPW	0	TEL:Microphone control output
12	RESET		Reset input(RESET)
13	XOUT		Clock output
14	VSS		Power supply input(Vss)
15	XIN		Clock input
16	VCC		Power supply input(Vcc)
17	NC		Not used(Vcc)(Pull up)
18	RCK		RDS:Clock input
19	LDET	i	RDS:PLL lock detection input
20	DALMON	Ö	DFS alarm output
21	RX2	1	IPBUS:Input 2
		0	
22	OELPW		OEL power supply output
23	SYSPW	0	System power control output
24	PEE	0	Beep tone output
25	RDS57K		RDS:57KHz pulse count input
26	ROMCS	0	External ROM:Chip select output
27	ROMCK	0	External ROM:Clock output
28	ROMDATA	1/0	External ROM:Data input / output
29	RX		IPBUS:Data input
30	TX	0	IPBUS:Data output
31-33	NC		Not used -
34	VDCONT	0	CD:VD power supply control output
35	DPDT	0	GRILLE:Display data output
36	KYDT	Ĭ	GRILLE:Key data input
37, 38	ROT1, 0	i	Rotary encoder pulse input 1, 0
39	PCL PCL	0	Clock adjustment output
	SWVDD		
40		0	GRILLE:Chip enable output
41	DSENS		Detach sense input
42	FLPILM	0	Inside of flap illumination output
43	ILMPW	0	Illumination output
44	EJTIN		EJECT key input
45	DRST	0	RDS:Reset output
46	RDSLK		RDS:Lock signal input
47	RDT		RDS:Data input
48	AM/FM	0	TUNER:Decoder power supply control output
49	ST	1	TUNER:Stereo input
50	SD	11	TUNER:SD input
51	NL2DT	ti	RDS:Noise level 2 input
52	TMUTE	Ö	RDS:Mute output
53	SDBW	i	RDS:In case of NF, SD input
		-	
54-57	NC		Not used
58	CONT	0	CD:Servo driver control output
59-61	XPIO3-1	1/0	CD:LSI data input/output 3-1
62	VCC		Power supply input(Vcc)
63	XPIO0	I/O	CD:LSI data input/output 0
64	VSS		Power supply input(Vss)
65	CDLOEJ	0	CD:Load Motor Load/Eject output

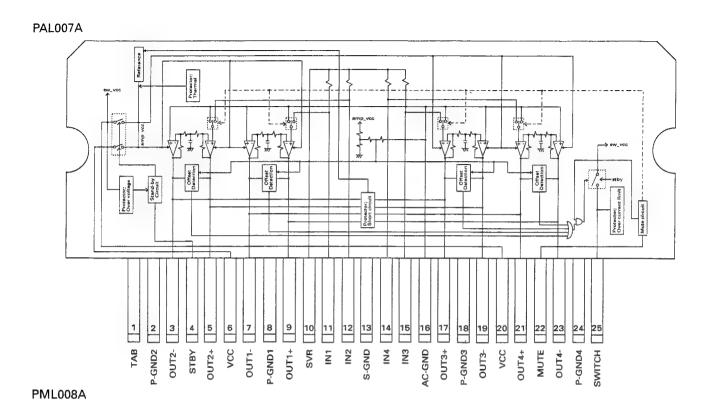
Pin No.	Pin Name	I/O	Function and Operation
66	CLCONT	0	CD:Driver input switch output
67	NC		Not used
68	CD5VON	0	CD:Power supply control output
69	HOME	I	CD:CRG HOME detection input
70	HTELM	0	TEL:Mute output for handsfree (Not used)
71	TUNPCE2	0	TUNER:Chip enable output(EEPROM)
72	TUNPCE	0	TUNER:Chip enable output(PLL)
73	BSENS		Backup sense
74	ASENS		ACC sense
75	CURRQ	0	RDS:Voltage FIX output
76	LOCH	0	TUNER:Local H output
77	LOCL	0	TUNER:Local L output
78	XPCK	0	CD:LSI clock output
79	XCE	0	CD:LSI chip enable output
80	XRST	0	CD:LSI reset output
81	IPPW	0	IPBUS:Driver power supply control output
82	ASENBO	0	IPBUS:Slave ACC sense output
83	ISENS	1	Illumination sense input
84, 85	MODEL1, 0	1	Model input 1, 0 (Not used)
86	RECIVE	0	During RDS data reception output
87	MUTE	0	Mute output
88	TESTIN	1	Test program input
89	DSCSNS	<u> </u>	CD:Disc position detection input
90	VDSENS	1	CD:VD power supply sense input
91	TEMP	1	CD:Temperature sense input
92	LVLINR	1	Level indicator R ch input
93	CSENS		Flap open/close sense input
94	LVLINL		Level indicator L ch input
95	NL1	1	RDS:Noise level input 1
96	AVSS		AD converter power supply input(Vss)
97	SL		TUNER:Signal level input
98	VREF	-	AD converter reference voltage(Vref)
99	AVCC		AD converter power supply input(Vcc)
100	TUNPDI		TUNER:Data input

### \*PD5700A



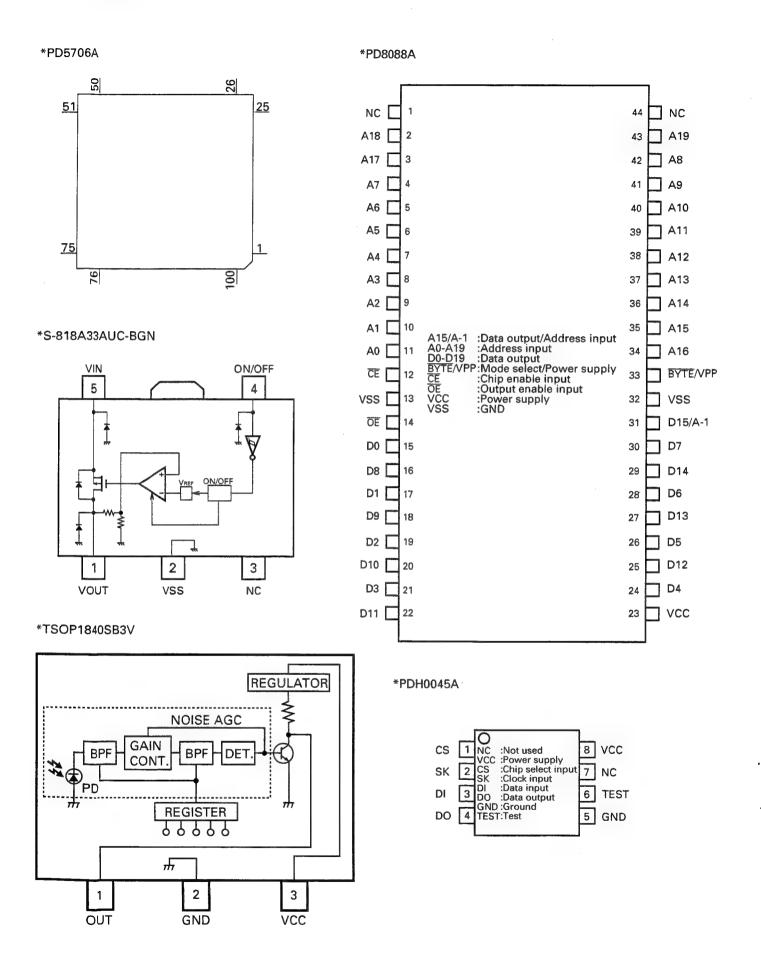
IC's marked by \* are MOS type.

Be careful in handling them because they are very liable to be damaged by electrostatic induction.



43 33 Sel\_out\_R Sel\_out\_L Isolator circuit Rch block (same as Lch) PV\_in\_L 2 32 PV\_in\_R Tone\_out\_FL 3 31 Tone\_out\_FR to Rch Primary volume LPF (L+R) SV\_in\_FL 4 30 SV\_in\_FR Loudness volume Anti Alias filter Tone\_out\_RL 5 29 Tone\_out\_RR SV\_in\_RL 6 28 SV\_in\_RR Anti radiatio NC 7 27 NC Anti radiation NC 8 26 NC AGND\_L 9 25 AGND\_R MUTE Zero cross detect circuit Front\_out\_L 10 24 Front\_out\_R Digital block Rear\_out\_L 11 23 Rear\_out\_R 13 Vref 2

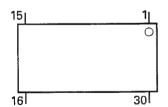
Pin No.		
5         REM         I         Remote control reception input           6         BYTE         I         GND connection           7         CNVSS         I         GND connection           8.9         NC         Not used OPEN           10         RESET         Pull up           11         XOUT         O         Crystal oscillating element connection pin           12         VSS         VSS connection           13         XIN         I         Crystal oscillating element connection pin           14         VCC         VCC connection           15         NMI         I         NVI input (Not used)           16-19         KDT-KD2         I         Key data 1-4 input           20         CKC         O         Fixed pulse output for cathode driver           21         NC         Not used           22         CKA         O         Fixed pulse output for anode driver           23         NC         Not used           24         LS         O         Line sink signal output           25         NC         Not used           26         CKD         O         Data transport / driver clock output           29         D		1 1
6         BYTE         I         GND connection           7         CNVSS         I         GND connection           8,9         NC         Not used OPEN           10         RESET         Pull up           11         XOUT         O         Crystal oscillating element connection pin           12         VSS         VSS connection           13         XIN         I         Crystal oscillating element connection pin           14         VCC         VCC connection           15         NMI         I         NVI input (Not used)           16-19         KD1-KD2         I         Key data 1-4 input           20         CKC         O         Fixed pulse output for cathode driver           21         NC         Not used           22         CKA         O         Fixed pulse output for anode driver           23         NC         Not used           24         LS         O         Line sink signal output           25         NC         Not used           26         CKD         O         Data transport / driver clock output           29         DA2         O         Display data input           29         DA2		1-4
7         CNVSS         I         GND connection           8,9         NC         Not used OPEN           10         RESET         Pull up           11         XOUT         O         Crystal oscillating element connection pin           12         VSS         VSS connection           13         XIN         I         Crystal oscillating element connection pin           14         VCC         VCC connection           15         NMI         I         NVI input (Not used)           16-19         KDT-KD2         I         Key data 1-4 input           20         CKC         O         Fixed pulse output for cathode driver           21         NC         Not used           22         CKA         O         Fixed pulse output for anode driver           23         NC         Not used           24         LS         O         Line sink signal output           25         NC         Not used           26         CKD         O         Data transport / driver clock output           27         DPDT         I         Display data input           29         DA2         O         Display data MSB output           30         NC		5
8,9 NC Not used OPEN  10 RESET Pull up  11 XOUT O Crystal oscillating element connection pin  12 VSS VSS connection  13 XIN I Crystal oscillating element connection pin  14 VCC VCC connection  15 NMI I NVI input (Not used)  16-19 KD1-KD2 I Key data 1-4 input  20 CKC O Fixed pulse output for cathode driver  21 NC Not used  22 CKA O Fixed pulse output for anode driver  23 NC Not used  24 LS O Line sink signal output  25 NC Not used  26 CKD O Data transport / driver clock output  27 DPDT I Display data input  28 KYDT O Key data output  29 DA2 O Display data MSB output  30 NC Not used  31 CLK1 I Clock input for UART1  32 ILMD O Dual illumination select output  33 DA1 O Display data LSB output  Not used  34 NC Not used  35 CLK0 I Clock input for UART1 input		6
10		7
11   XOUT   O   Crystal oscillating element connection pin		8, 9
11   XOUT   O   Crystal oscillating element connection pin		
12		
14         VCC         VCC connection           15         NMI         I         NVI input (Not used)           16-19         KDT-KD2         I         Key data 1-4 input           20         CKC         O         Fixed pulse output for cathode driver           21         NC         Not used           22         CKA         O         Fixed pulse output for anode driver           23         NC         Not used           24         LS         O         Line sink signal output           25         NC         Not used           26         CKD         O         Data transport / driver clock output           27         DPDT         I         Display data input           28         KYDT         O         Key data output           29         DA2         O         Display data MSB output           30         NC         Not used           31         CLK1         I         Clock input for UART1           32         ILMD         O         Dual illumination select output           33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0		12
14         VCC         VCC connection           15         NMI         I         NVI input (Not used)           16-19         KD1-KD2         I         Key data 1-4 input           20         CKC         O         Fixed pulse output for cathode driver           21         NC         Not used           22         CKA         O         Fixed pulse output for anode driver           23         NC         Not used           24         LS         O         Line sink signal output           25         NC         Not used           26         CKD         O         Data transport / driver clock output           27         DPDT         I         Display data input           28         KYDT         O         Key data output           29         DA2         O         Display data MSB output           30         NC         Not used           31         CLK1         I         Clock input for UART1           32         ILMD         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		13
15 NMI		
16-19   KD1-KD2		
20 CKC O Fixed pulse output for cathode driver 21 NC Not used 22 CKA O Fixed pulse output for anode driver 23 NC Not used 24 LS O Line sink signal output 25 NC Not used 26 CKD O Data transport / driver clock output 27 DPDT I Display data input 28 KYDT O Key data output 29 DA2 O Display data MSB output 30 NC Not used 31 CLK1 I Clock input for UART1 32 ILMD O Dual illumination select output 33 DA1 O Display data LSB output 34 NC Not used 35 CLK0 I Clock input for UART0 input		
21 NC Not used 22 CKA O Fixed pulse output for anode driver 23 NC Not used 24 LS O Line sink signal output 25 NC Not used 26 CKD O Data transport / driver clock output 27 DPDT I Display data input 28 KYDT O Key data output 29 DA2 O Display data MSB output 30 NC Not used 31 CLK1 I Clock input for UART1 32 ILMD O Dual illumination select output 33 DA1 O Display data LSB output 34 NC Not used 35 CLK0 I Clock input for UART0 input		
22 CKA O Fixed pulse output for anode driver 23 NC Not used 24 LS O Line sink signal output 25 NC Not used 26 CKD O Data transport / driver clock output 27 DPDT I Display data input 28 KYDT O Key data output 29 DA2 O Display data MSB output 30 NC Not used 31 CLK1 I Clock input for UART1 32 ILMD O Dual illumination select output 33 DA1 O Display data LSB output 34 NC Not used 35 CLK0 I Clock input for UART0 input		
23         NC         Not used           24         LS         O         Line sink signal output           25         NC         Not used           26         CKD         O         Data transport / driver clock output           27         DPDT         I         Display data input           28         KYDT         O         Key data output           29         DA2         O         Display data MSB output           30         NC         Not used           31         CLK1         I         Clock input for UART1           32         ILMD         O         Dual illumination select output           33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		
24         LS         O         Line sink signal output           25         NC         Not used           26         CKD         O         Data transport / driver clock output           27         DPDT         I         Display data input           28         KYDT         O         Key data output           29         DA2         O         Display data MSB output           30         NC         Not used           31         CLK1         I         Clock input for UART1           32         ILMD         O         Dual illumination select output           33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		
25         NC         Not used           26         CKD         O         Data transport / driver clock output           27         DPDT         I         Display data input           28         KYDT         O         Key data output           29         DA2         O         Display data MSB output           30         NC         Not used           31         CLK1         I         Clock input for UART1           32         ILMD         O         Dual illumination select output           33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		
26 CKD O Data transport / driver clock output 27 DPDT I Display data input 28 KYDT O Key data output 29 DA2 O Display data MSB output 30 NC Not used 31 CLK1 I Clock input for UART1 32 ILMD O Dual illumination select output 33 DA1 O Display data LSB output 34 NC Not used 35 CLK0 I Clock input for UART0 input		
27         DPDT         I         Display data input           28         KYDT         O         Key data output           29         DA2         O         Display data MSB output           30         NC         Not used           31         CLK1         I         Clock input for UART1           32         ILMD         O         Dual illumination select output           33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		
28         KYDT         O         Key data output           29         DA2         O         Display data MSB output           30         NC         Not used           31         CLK1         I         Clock input for UART1           32         ILMD         O         Dual illumination select output           33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		
29         DA2         O         Display data MSB output           30         NC         Not used           31         CLK1         I         Clock input for UART1           32         ILMD         O         Dual illumination select output           33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		
30 NC		
31         CLK1         I         Clock input for UART1           32         ILMD         O         Dual illumination select output           33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		
32 ILMD O Dual illumination select output 33 DA1 O Display data LSB output 34 NC Not used 35 CLK0 I Clock input for UART0 input		
33         DA1         O         Display data LSB output           34         NC         Not used           35         CLK0         I         Clock input for UART0 input		
34 NC Not used 35 CLK0 I Clock input for UART0 input		
35 CLK0 I Clock input for UART0 input		
	<u> </u>	
36 NC Not used		
37 RDY I Not used		
38 NC Not used		
39 HOLD I Not used		
40,41 NC Not used		
42 RD O Read strobe output		
43-46 NC Not used		
47 CS1 O Bank address select output		
48 CSO O External ROM chip select output	-	
49 A19 O Address bus 19 output		
50 A18 O Address bus 18 output (Not used)		
51-59 A17-A9 O Address bus 17-9 output		
60 VCC VCC connection		
61 A8 O Address bus 8 output		
62 VSS GND connection		
63-69 A7-A1 O Address bus 7-1 output		
70 A0 O Address bus 0 output (Not used)		
71-86 D15-D0 I/O Data bus 15-0 input / output		
87-92 KS1-KS6 I/O Key strobe input / output		
93 NC Not used		
94 AVSS GND connection		
95 NC Not used OPEN		
		95
96 VREF Not used VSS connection		
		96 97



● Pin Functions(TA2153FN)

Pin No. Pin Name I/O Function and Operation  1 VCC Power supply voltage terminal  2 RFGC I RF amplitude adjustment control signal terminal  3 GMAD I AGC amplifier frequency characteristic adjustment terminal  4 FNI I Main beam amplifier input terminal  5 FPI I Main beam amplifier input terminal  6 TPI I Sub beam amplifier input terminal  7 TNI I Sub beam amplifier input terminal  8 MDI O Monitor photodiode amplifier input terminal  9 LDO I Laser diode amplifier output terminal  10 SEL I APC circuit ON/OFF signal, LDO terminal control input terminal and peak detection frequency switching terminals  11 TEB I Tracking error balance adjustment signal input terminal  12 2VRO O Reference voltage (2VRO) output terminal  13 TEN I Tracking error signal generation amplifier output terminal  14 TEO O Tracking error signal generation amplifier output terminal  16 FEO O Focus error signal generation amplifier output terminal  17 FEN I Focus error signal generation amplifier reverse phase input terminal  18 SEB I RFRP generation circuit mode switching terminal  19 VRO O Reference voltage (VREF) output terminal  20 RFRP O Signal generation amplifier output terminal  21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation amplifier output terminal  22 RFCT O RFRP signal center level output terminal for RFCT signal generation amplifier input terminal  23 PKC I Peak detection time constant adjustment terminal for RFCT signal generation amplifier input terminal  24 RFRPIN I Signal generation amplifier input terminal  25 RFGO O RF signal amplitude adjustment amplifier input terminal  26 GVSW I AGC, FE or TE amplifier input terminal  27 AGCIN I RF signal amplitude adjustment amplifier input terminal  28 RFO O RF signal generation amplifier input terminal  29 GND I RF signal generation amplifier input terminal	<u> </u>	ons(IAZ ISSFI		
2 RFGC I RF amplitude adjustment control signal terminal 3 GMAD I AGC amplifier frequency characteristic adjustment terminal 4 FNI I Main beam amplifier input terminal 5 FPI I Sub beam amplifier input terminal 6 TPI I Sub beam amplifier input terminal 7 TNI I Sub beam amplifier input terminal 8 MDI O Monitor photodiode amplifier input terminal 9 LDO I Laser diode amplifier output terminal 10 SEL I APC circuit ON/OFF signal, LDO terminal control input terminal and peak detection frequency switching terminals 11 TEB I Tracking error balance adjustment signal input terminal 12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 20 RFRP O Signal generation amplifier output terminal 21 RFRP GO Signal generation amplifier output terminal 22 RFRP O Signal generation amplifier output terminal 23 PKC I Bottom detection time constant adjustment terminal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal 25 RFGO O RFR signal amplitude adjustment signal for RFCT signal generation 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal amplitude adjustment amplifier input terminal 29 GND I GND terminal	Pin No.		I/O	Function and Operation
AGC amplifier frequency characteristic adjustment terminal	1			
4 FNI I Main beam amplifier input terminal 5 FPI I Sub beam amplifier input terminal 6 TPI I Sub beam amplifier input terminal 7 TNI I Sub beam amplifier input terminal 8 MDI O Monitor photodiode amplifier input terminal 9 LDO I Laser diode amplifier output terminal 10 SEL I APC circuit ON/OFF signal, LDO terminal control input terminal and peak detection frequency switching terminals 11 TEB I Tracking error balance adjustment signal input terminal 12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier reverse phase input terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 28 RFO O RF signal generation amplifier input terminal	2	RFGC	1	
5 FPI I Main beam amplifier input terminal 6 TPI I Sub beam amplifier input terminal 7 TNII I Sub beam amplifier input terminal 8 MDI O Monitor photodiode amplifier input terminal 9 LDO I Laser diode amplifier output terminal 10 SEL I APC circuit ON/OFF signal, LDO terminal control input terminal and bottom and peak detection frequency switching terminals 11 TEB I Tracking error balance adjustment signal input terminal 12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal	3	GMAD	1	
6 TPI I Sub beam amplifier input terminal 7 TNI I Sub beam amplifier input terminal 8 MDI O Monitor photodiode amplifier input terminal 9 LDO I Laser diode amplifier output terminal 10 SEL I APC circuit ON/OFF signal, LDO terminal control input terminal and peak detection frequency switching terminals 11 TEB I Tracking error balance adjustment signal input terminal 12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal for RFCT signal generation 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal	4	FNI	1	Main beam amplifier input terminal
7 TNI I Sub beam amplifier input terminal 8 MDI O Monitor photodiode amplifier input terminal 9 LDO I Laser diode amplifier output terminal 10 SEL I APC circuit ON/OFF signal, LDO terminal control input terminal and bottom and peak detection frequency switching terminals 11 TEB I Tracking error balance adjustment signal input terminal 12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier reverse phase input terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal	5	FPI	1	Main beam amplifier input terminal
8 MDI O Monitor photodiode amplifier input terminal 9 LDO I Laser diode amplifier output terminal 10 SEL I APC circuit ON/OFF signal, LDO terminal control input terminal and peak detection frequency switching terminals 11 TEB I Tracking error balance adjustment signal input terminal 12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GYSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal	6	TPI	I	Sub beam amplifier input terminal
9 LDO I Laser diode amplifier output terminal 10 SEL I APC circuit ON/OFF signal, LDO terminal control input terminal and bottom and peak detection frequency switching terminals 11 TEB I Tracking error balance adjustment signal input terminal 12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal amplitude adjustment amplifier input terminal 29 GND I GND terminal	7	TNI	ı	
APC circuit ON/OFF signal, LDO terminal control input terminal and bottom and peak detection frequency switching terminals	8	MDI	0	Monitor photodiode amplifier input terminal
and peak detection frequency switching terminals  11 TEB   I Tracking error balance adjustment signal input terminal  12 2VRO   O Reference voltage (2VRO) output terminal  13 TEN   I Tracking error signal generation amplifier reverse phase input terminal  14 TEO   O Tracking error signal generation amplifier output terminal  15 SBAD   O Sub beam addition signal output terminal  16 FEO   O Focus error signal generation amplifier output terminal  17 FEN   I Focus error signal generation amplifier reverse phase input terminal  18 SEB   I RFRP generation circuit mode switching terminal  19 VRO   O Reference voltage (VREF) output terminal  20 RFRP   O Signal generation amplifier output terminal for track count  21 BTC   Bottom detection time constant adjustment terminal for RFCT signal generation  22 RFCT   O RFRP signal center level output terminal  23 PKC   I Peak detection time constant adjustment signal for RFCT signal generation  24 RFRPIN   I Signal generation amplifier input terminal for track count  25 RFGO   O RF signal amplitude adjustment amplifier output terminal  26 GVSW   I AGC, FE or TE amplifier gain switching terminal  27 AGCIN   I RF signal amplitude adjustment amplifier input terminal  28 RFO   O RF signal generation amplifier output terminal  29 GND   I GND terminal	9	LDO	l i	Laser diode amplifier output terminal
11 TEB I Tracking error balance adjustment signal input terminal 12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal	10	SEL	ı	APC circuit ON/OFF signal, LDO terminal control input terminal and bottom
12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal				and peak detection frequency switching terminals
12 2VRO O Reference voltage (2VRO) output terminal 13 TEN I Tracking error signal generation amplifier reverse phase input terminal 14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal	11	TEB	1	Tracking error balance adjustment signal input terminal
14 TEO O Tracking error signal generation amplifier output terminal 15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal		2VRO	0	Reference voltage (2VRO) output terminal
15 SBAD O Sub beam addition signal output terminal 16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal	13	TEN	Ī	Tracking error signal generation amplifier reverse phase input terminal
16 FEO O Focus error signal generation amplifier output terminal 17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal	14	TEO	0	Tracking error signal generation amplifier output terminal
17 FEN I Focus error signal generation amplifier reverse phase input terminal 18 SEB I RFRP generation circuit mode switching terminal 19 VRO O Reference voltage (VREF) output terminal 20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal	15	SBAD	0	Sub beam addition signal output terminal
18 SEB	16	FEO	0	Focus error signal generation amplifier output terminal
18 SEB	17	FEN	1	Focus error signal generation amplifier reverse phase input terminal
20 RFRP O Signal generation amplifier output terminal for track count 21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation 22 RFCT O RFRP signal center level output terminal 23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal	18	SEB	1	
21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation  22 RFCT O RFRP signal center level output terminal  23 PKC I Peak detection time constant adjustment signal for RFCT signal generation  24 RFRPIN I Signal generation amplifier input terminal for track count  25 RFGO O RF signal amplitude adjustment amplifier output terminal  26 GVSW I AGC, FE or TE amplifier gain switching terminal  27 AGCIN I RF signal amplitude adjustment amplifier input terminal  28 RFO O RF signal generation amplifier output terminal  29 GND I GND terminal	19	VRO	0	Reference voltage (VREF) output terminal
21 BTC I Bottom detection time constant adjustment terminal for RFCT signal generation  22 RFCT O RFRP signal center level output terminal  23 PKC I Peak detection time constant adjustment signal for RFCT signal generation  24 RFRPIN I Signal generation amplifier input terminal for track count  25 RFGO O RF signal amplitude adjustment amplifier output terminal  26 GVSW I AGC, FE or TE amplifier gain switching terminal  27 AGCIN I RF signal amplitude adjustment amplifier input terminal  28 RFO O RF signal generation amplifier output terminal  29 GND I GND terminal	20	RFRP	0	Signal generation amplifier output terminal for track count
generation  22 RFCT O RFRP signal center level output terminal  23 PKC I Peak detection time constant adjustment signal for RFCT signal generation  24 RFRPIN I Signal generation amplifier input terminal for track count  25 RFGO O RF signal amplitude adjustment amplifier output terminal  26 GVSW I AGC, FE or TE amplifier gain switching terminal  27 AGCIN I RF signal amplitude adjustment amplifier input terminal  28 RFO O RF signal generation amplifier output terminal  29 GND I GND terminal				Bottom detection time constant adjustment terminal for RFCT signal
23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal				generation
23 PKC I Peak detection time constant adjustment signal for RFCT signal generation 24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal	22	RFCT	0	RFRP signal center level output terminal
24 RFRPIN I Signal generation amplifier input terminal for track count 25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal			I	Peak detection time constant adjustment signal for RFCT signal generation
25 RFGO O RF signal amplitude adjustment amplifier output terminal 26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal			1	
26 GVSW I AGC, FE or TE amplifier gain switching terminal 27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal			0	
27 AGCIN I RF signal amplitude adjustment amplifier input terminal 28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal				
28 RFO O RF signal generation amplifier output terminal 29 GND I GND terminal			1	
29 GND I GND terminal			0	
			1	
			ı	RF signal generation amplifier input terminal

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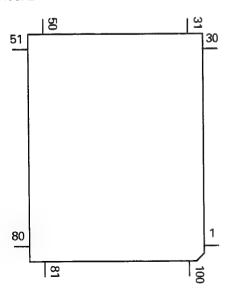


● Pin Functions(TC9495F2)

		ons(TC9495F2	<del>· • • • • • • • • • • • • • • • • • • •</del>	
Pin		Pin Name	1/0	Function and Operation
	1	TESTO		Test mode terminal
	2	HSO	0	Replay speed flag output terminal
	3	UHSO	0	Replay speed flag output terminal
	4	EMPH	0	Emphasis flag output terminal for sub code Q data
	_ 5	LRCK	0	Channel clock (44.1 kHz) output terminal
	6	VSS		Digital ground terminal
	_ 7	BCK	0	Bit clock output terminal
	_ 8	AOUT	0	Digital audio data output terminal
	9	DOUT	0	Digital out output terminal
	10	MBOV	0	Buffer memory over signal output terminal
	11	IPF	0	Correction flag output terminal
	12	SBOK	0	CRCC decision result output for sub code Q data
	13	CLCK	1/0	Clock input/output terminal for sub code P-W data read
	14	VDD		Digital + power supply terminal (5 V)
	15	VSS		Digital ground terminal
	16	DATA	0	Sub code P-W data output terminal
	17	SFSY	0	Replay-system frame sync signal output terminal
	18	SBSY	0	Sub code block sync output terminal
	19	SPCK	0	Clock for processor status signal read
	20	SPDA	0	Processor status signal output terminal
	21	COFS	0	Correction-system frame clock (7.35 kHz) output terminal
	22	MONIT	0	LSI internal signal output terminal
	23	VDD		Digital + power supply terminal (5 V)
	24	TESIO0	1	Test input/output terminal
	25	P2VREF		PLL-system only 2VREF terminal
	26	HSSW	0	The VREF voltage is reached for double or quad speed.
	27	ZDET	0	One-bit DAC zero detection flag output terminal
	28	PDO	0	Phase error signal issue between the EFM and PLCK signals
	29	TMAXS	0	TMAX detection result output terminal
	30	TAMX	0	TMAX detection result output terminal
	31	LPFN	1	Reverse input terminal of amplifier for lowpass filter
	32	LPFO	0	Output terminal of amplifier for lowpass filter
	33	PVREF		PLL-system only VREF terminal
	34	VCOREF		VCO center frequency reference level terminal
	35	VCOF	0	Filter terminal for VCO
	36	AVSS		Analog-system ground terminal
	37	SLCO	0	Output terminal of DAC for data slice level generation
	38	RFI	1	RF signal input terminal
	39	AVDD		Analog-system power supply terminal (5 V)
	40	RFCT		RFRP signal center level input terminal
	41	RFZI	1	Input terminal for RFRP signal zero cross
	42	RFRP	1	RF ripple signal input terminal
	43	FEI		Focus error signal input terminal
	44	SBAD	1	Sub beam addition signal input terminal
	45	TSIN	1	Test input terminal
	46	TEI	1	Tracking error input terminal
	47	TEZI	1	Input terminal for tracking error or zero cross
	48	FOO	0	Focus equalizer output terminal
	49	TRO	0	Tracking equalizer output terminal
	50	VREF		Analog reference power supply terminal
	51	RFGC	0	RF amplitude adjustment control signal output terminal
	52	TEBC	0	Tracking balance control signal output terminal
	53	FMO	0	Feed equalizer output terminal
	54	FVO	0	Speed error signal or feed search EQ output
	55	DMO	0	Disc equalizer output terminal
	56	2VREF		Analog reference power supply terminal
	57	SEL	0	APC circuit ON/OFF signal output terminal
	57	SEL	10	APC circuit ON/OFF signal output terminal

Pin No.	Pin Name	1/0	Function and Operation
58-61	FLGA-D	0	External flag output terminal for internal signal monitor
62	VDD		Digital + power supply terminal (5 V)
63	VSS		Digital ground terminal
64	100	0	RF amplifier gain switching terminal
65	101	0	Not used
66	102	I	HOME detection switch input terminal
67	103	0	FocusDrv and signal output terminal
68	DMOUT		Field equalizer PWM output terminal for IO0 and IO1
			Disc equalizer PWM output terminal for IO2 and IO3
69	CKSE	1	Usually open
70	DACT	I	DAC test mode terminal
71	TESIN	I	Test input terminal
72	TESIO1		Test input/output terminal
73	VSS		Digital ground terminal
74	PXI	1	DPS-system clock oscillator circuit input terminal
75	PXO	0	DPS-system clock oscillator circuit output terminal
76	VDD		Digital + power supply terminal (5 V)
77	XVSS		Ground terminal for system clock oscillator circuit
78	XI	I	System clock oscillator circuit input terminal
79	XO	0	System clock oscillator circuit output terminal
80	XVDD		For system clock oscillator circuit + power supply terminal
81	DVSR		R channel D/A converting unit power supply terminal
82	RO	0	R channel data forward rotation output terminal
83	DVDD		D/A converting unit power supply terminal (5 V)
84	DVR		Reference voltage terminal
85	LO	0	L channel forward rotation output terminal
86	DVSL		L channel D/A converting unit power supply terminal
87-89	TEST1-3	1	Test mode terminal
90-93	BUS0-3	1/0	Data input/output terminal for microcomputer interface
94	VDD		Digital + power supply terminal (5 V)
95	VSS		Digital ground terminal
96	BUCK	1	Clock terminal for microcomputer interface
97	CEE	I	Chip enable signal for microcomputer interface
98	TEST4	1	Test mode terminal
99	TSMOD	1	Test mode terminal
100	RST		Reset signal input terminal

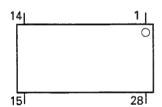
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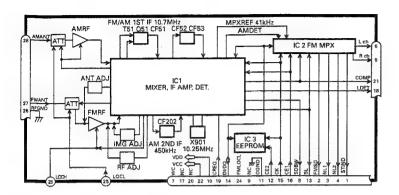
● Pin Functions(BA5996FM)

Pin No.	Pin Name	Function and Operation	
1	VR	Input pin for reference voltage	
2	OPIN2(+)	Input pin for non-inverting input for CH2 preamplifier	
3	OPIN2(-)	Input pin for inverting input for CH2 preamplifier	
4	OPOUT2	Output pin for CH2 preamplifier	
5	OPIN1(+)	Input pin for non-inverting input for CH1 preamplifier	
6	OPIN1(-)	nput pin for inverting input from CH1 preamplifier	
7	OPOUT1	Output pin for CH1 preamplifier	
8	GND	Ground pin	
9	MUTE	Mute control pin	
10	POWVCC1	Power supply pin for CH1, CH2, and CH3 at "Power" stage	
11	VO1(-)	Driver CH1 - Negative output	
12	VO1(+)	Driver CH2 - Positive output	
13	VO2(-)	Driver CH2 - Negative output	
14	VO2(+)	Driver CH2 - Positive output	
15	VO3(+)	Driver CH2 - Positive output	
16	VO3(-)	Driver CH2 - Negative output	
17	VO4(+)	Driver CH4 - Positive output	
18	VO4(-)	Driver CH4 - Negative output	
19	POWVCC2	Power supply pin for CH4 at "Power" stage	
20	GND	Ground pin	
21	CNT	Control pin	
22	LDIN	Loading input	
23	OPOUTSL	Output pin for preamplifier for thread	
24	OPINSL	Input pin for preamplifier for thread	
25	OPOUT3	CH3 preamplifier output pin	
26	OPIN3(-)	Input pin for inverting input for CH3 preamplifier	
27	OPIN3(+)	Input pin for non-inverting input for CH3 preamplifier	
28	PREVCC	PreVcc ·	

### BA5996FM

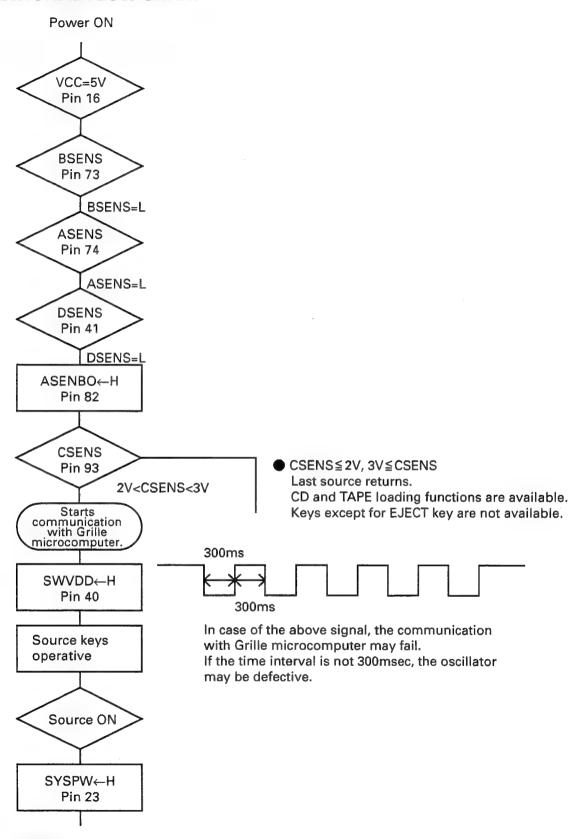


### ● FM/AM Tuner Unit



No.	Symbol	1/0	Explain		
	1 STIND O stereo "Low" when the FM stereo signals are received.				
indicator			To be pulled up to the "VDD" at $47k\Omega$ .		
2	2 FMSD O FM station			"High" when signals are received. To be pulled up to the "VDD" at $47k\Omega$	
_			detector	Meanwhile, $10k\Omega$ should be used when taking diver FIX trigger from here	
				and "High: 0.9VDD or more" and "Low: 250mV or less".	
٠ .		ĺ		(Should satisfy the diver IC specifications)	
3	NL1	0	noise level-1	"High" when noise is received. Output for the RDS. GND at $47k\Omega$ //1,800pF.	
	NL2	ō	noise level-2	"High" when noise is received. Output for the RDS. GND at $36k\Omega$ //330pF.	
	Rch	ō	R channel	FM stereo "R-ch" signal output or AM audio output.	
			output	Add the specified de-emphasis constant.	
6	Lch	0	L channel	FM stereo "L-ch" signal output or AM audio output.	
			output	Add the specified de-emphasis constant.	
7	WC		write control	EEPROM write control. Writing permissible at "Low". Normally open.	
	SDBW	0	SD bandwidth	SD bandwidth signal output. For detection of detuning data for the RDS.	
	NC			Not used	
	VDD		power	Power supply pin for the digital section.	
			supply	DC 5V +/- 0.25V. Be careful about overlapping noise in the logic section.	
11	DGND		digital ground	Grounding for the digital section.	
	CE2		chip enable-2	EEPROM chip enable. Active a "Low"	
				To be pulled up to the "VDD" at $47k\Omega$	
13	SL	1/0	signal level	Received FM/AM signal level (strength) output.	
		, -		Connect the specified load resistor and capacitor (10k $\Omega$ + 39k $\Omega$ //4,700pF)	
14	DI/DO	1/0	data input/	Data input/Data output	
			data output	To be pulled up to the "VDD" at 47kΩ	
15	CK		clock	Clock input To be pulled up to the "VDD" at 47kΩ	
16	CE1		chip enable-1	AF-RF chip enable. Active at "High" To be grounded at 47kΩ	
17	NC			Not used	
	LDET	0	lock detector	Active at "Low". To be pulled up to the "VDD" at $47k\Omega$	
19	CREQ	1	current request	Active at "Low". To be grounded at 47kΩ	
20	NC			Not used	
21	COMP	0	composite signal	FM composite signal output. $r$ out $< 100\Omega$	
	VCC		power supply	Analog section power supply pin.DC 8.4V +/- 0.3V	
23	LOCH		local high	FM local high pin. When seeking local high, apply 5V together with "LOCL".	
24	FMLOCL	1	FM local low	FM local low pin. When seeking local low, apply 5V to the base of the NPN	
				transistor with which the specified resistor is being connected to the emitter	
				Keep it open in case of ordinary marketed models.	
25			FM/AM local low pin. When seeking local low, apply 5V to the base of the		
				NPN transistor. Since this pin is exclusive for AM when the FMLOCL is in use,	
				do not drive it under FM.	
26	RFGND		RF ground	Grounding for the antenna section.	
27	FMANT	Ĭ.	FM antenna input		
28	AMANT	1	AM antenna input	AM antenna input. High impedance.	
				Connect to the antenna through an L (LAU type) of 4.7µH.To cope with the	
				power transmission line hums, insert a series circuit consisting of an L	
				(a coil of about 100mH) + R (a resistor of 470 $\Omega$ to 2.2k $\Omega$ ) between the GND.	

### 7.3 OPERATIONAL FLOW CHART



Completes power-on operation. (After that, proceed to each source operation)

# 7.4 CLEANING

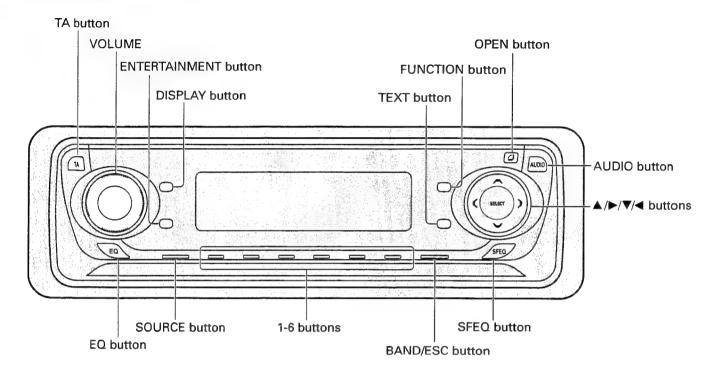
Before shipping out the product, be sure to clean the following portions by using the prescribed cleaning tools:

Portions to be cleaned	Cleaning tools
CD pickup lenses	Cleaning liquid : GEM1004
	Cleaning paper : GED-008

### 8. OPERATIONS AND SPECIFICATIONS

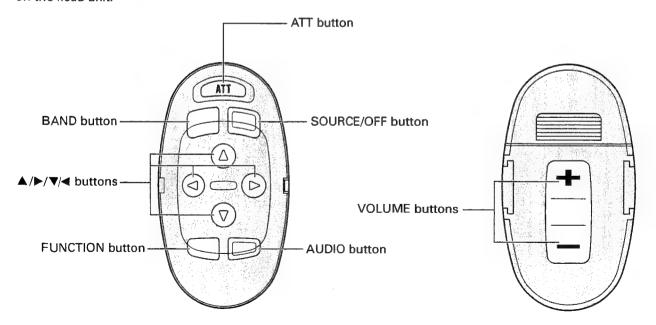
### 8.1 OPERATIONS

### **HEAD UNIT**

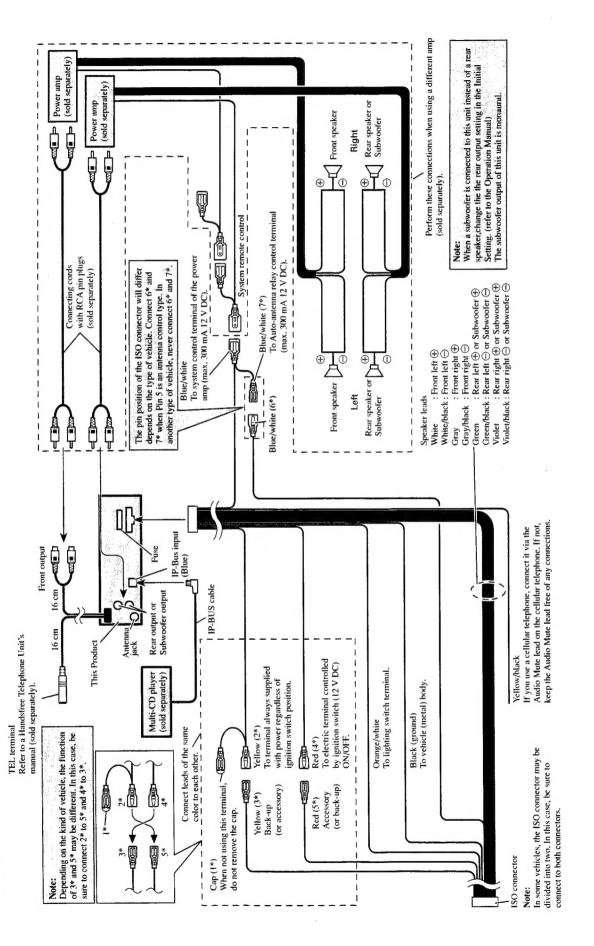


### **STEERING REMOTE CONTROL UNIT (CD-SR80)**

The steering remote control CD-SR80 is sold separately. Operation is the same as when using the button on the head unit.



# CONNECTION DIAGRAM



### **8.2 SPECIFICATIONS**

General	Tone controls:
Power source 14.4 V DC	(Bass)
(10.8 – 15.1 V allowable)	Frequency 40/63/100/160 Hz
Grounding system Negative type	Gain±12 dB
Max. current consumption	(Treble)
	Frequency 2.5 k/4 k/6.3 k/10 k Hz
10.0 A	Gain±12 dB
Backup current 5 mA or less	Subwoofer:
Dimensions (W x H x D):	Frequency 50/80/125 Hz
Chassis (DIN) 178 x 50 x 157 mm	Slope12 dB/oct
Nose 188 x 58 x 19 mm	
Weight 1.4 kg	Gain±12 dB
Audio	CD player
Maximum power output 50 W x 4	System Compact disc audio
for subwoofer (70 W x 1 ch/2 Ω)	system
	Usable discs Compact disc
25 W x 4 (BRI)	Signal format:
Continuous power output 27 W x 4	Sampling frequency 44.1 kHz
(DIN 45324, +B=14.4 V)	Number of quantization bits
Load impedance $4 \Omega (4-8 \Omega [2 \Omega \text{ for 1 ch}]$	
allowable)	16; linear
Preout max output level/output impedance	Frequency characteristics
2.2 V/1 kΩ	5 – 20,000 Hz (±1 dB)
Equalizer (3-Band Parametric Equalizer):	Signal-to-noise ratio . 94 dB (1 kHz)
(Low)	(IEC-A network)
Frequency 40/80/100/160 Hz	Dynamic range 92 dB (1 kHz)
Q Factor 0.35/0.59/0.95/1.15	Number of channels 2 (stereo)
(+6 dB when boosted)	
Gain±12 dB	FM tuner
	Frequency range 87.5 – 108.0 MHz
(Mid)	Usable sensitivity 9 dBf (0.8 $\mu$ V/75 $\Omega$ , mono,
Frequency 200/500/1 k/2 k Hz	S/N: 30 dB)
Q Factor 0.35/0.59/0.95/1.15	
(+6 dB when boosted)	50 dB quieting sensitivity 15 dBf (1.5 $\mu$ V/75 $\Omega$ , mono)
Gain±12 dB	Signal-to-noise ratio . 70 dB (IEC-A network)
(High)	Distortion 0.3% (at 65 dBf, 1 kHz,
Frequency 3.15 k/8 k/10 k/12.5 k Hz	stereo)
Q Factor 0.35/0.59/0.95/1.15	Frequency response 30 – 15,000 Hz (±3 dB)
(+6 dB when boosted)	Stereo separation 40 dB (at 65 dBf, 1 kHz)
Gain±12 dB	
Loudness contour:	MW tuner
(Low)+3.5 dB (100 Hz)	Frequency range 531 - 1,602 kHz (9 kHz)
+3 dB (10 kHz)	Usable sensitivity 18 µV (S/N: 20 dB)
(Mid)+10 dB (100 Hz)	Selectivity 50 dB (±9 kHz)
+6.5 dB (10 kHz)	Colocularly
(High)+11 dB (100 Hz)	I W tunos
+11 dB (100 Hz)	LW tuner
	Frequency range 153 – 281 kHz
(Volume: -30 dB)	Usable sensitivity 30 μV (S/N: 20 dB)
	Selectivity 50 dB (±9 kHz)

### Note

• Specifications and the design are subject to possible modifications without notice due to improvements.

# Pioneer sound.vision.soul



PION -06009

# Service Manual

ORDER NO. CRT2830

DEH-P6400R

X1B/EW



This service manual should be used together with the manual(s) listed below.
For the parts numbers, adjustments, etc. which are not shown in this manual, refer to the following manual(s).

Model No.	Order No.	Mech. Module	Remarks
DEH-P6400R/XN/EW	CRT2806		5992
CX-977	CRT2624	S9	CD Mechanism Module:Circuit Description, Mech.Description, Disassembly

### **EXPLODED VIEWS AND PARTS LIST**

PACKING (Page 4)

PACKING SECTION PARTS LIST

			Part No.	
Mark	No.	Description	DEH-P6400R/XN/EW	DEH-P6400R/X1B/EW
	1	Cord Assy	CDE6435	UDE6435
	2	Accessory Assy	CEA3062	UEA3062
*	8	Polyethylene Bag	E36-615	CEG-127
	9	Polyethylene Bag	CEG-162	UEG-012
	10	Carton	CHG4628	UHG4628
	11	Contain Box	CHL4628	UHL4628
	12	Protector	CHP2251	UHP2102
	13	Protector	CHP2252	UHP2101
	14	Inner Box	CHW1754	UHW1754
	15-1	Owner's Manual	CRD3513	URD-195
	15-2	Owner's Manual	CRD3514	URD-196
	15-3	Owner's Manual	CRD3515	URD-197
	15-4	Installation Manual	CRD3529	URD-198
*	15-5	Caution Card	CRP1207	URP1207
*	15-6	Passport	CRY1013	Not used
*	15-7	Warranty Card	CRY1157	URY1087
	15-8	Polyethylene Bag	CEG1116	UEG1116
	16	Case Assy	CXB3520	UXB-009

EXTERIOR (Page 6)

● EXTERIOR SECTION PARTS LIST

			Part No.	
Mark	No.	Description	DEH-P6400R/XN/EW	DEH-P6400R/X1B/EW
	5	Cord Assy	CDE6435	UDE6435
	11	Case	CNB2686	UNB2686
	13	Holder	CNC8659	UNC8659
	18	Tuner Amp Unit	CWM7984	UWM7984
	36	FM/AM Tuner Unit	CWE1562	UWE1562
	60	Detach Grille Assy	CXB7914	UXB7914
	80	Keyboard Unit	CWM7990	UWM7990

## **ELECTRICAL PARTS LIST(Page 32)**

Tuner Amp Unit

	Part No.	
Symbol and Description	DEH-P6400R/XN/EW	DEH-P6400R/X1B/EW
Q101, 951 Transistor	2SA1037K	2SA1162